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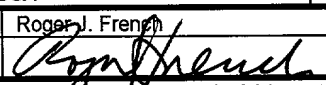
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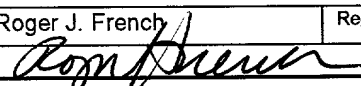
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UTILITY PATENT APPLICATION TRANSMITTAL (Only for new nonprovisional applications under 37 CFR 1.53(b))	Attorney Docket No.	47176-00538		
	First Inventor	Breck W. Lovinggood et al.		
	Title	INTEGRATED REPEATER		
	Express Mail Label No.	EK506617276US		
APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents.		ADDRESS TO: Assistant Commissioner for Patents Box Patent Application Washington, DC 20231		
<div>1. <input checked="" type="checkbox"/> Fee Transmittal Form (Submit an original, and a duplicate for fee processing)</div> <div>2. <input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27.</div> <div>3. <input checked="" type="checkbox"/> Specification [Total Pages <u>36</u>] (preferred arrangement set forth below)<ul style="list-style-type: none">- Descriptive title of the Invention- Cross References to Related Applications- Statement Regarding Fed sponsored R & D Invention- Reference to Microfiche Appendix- Background of the Invention- Brief Summary of the Invention- Brief Description of the Drawings (if filed)- Detailed Description- Claim(s)- Abstract of the Disclosure</div> <div>4. <input checked="" type="checkbox"/> Drawing(s) (35 USC 113) [Total Sheets <u>27</u>]</div> <div>5. <input checked="" type="checkbox"/> Oath or Declaration [Total Pages <u>3</u>]<ul style="list-style-type: none">a. <input checked="" type="checkbox"/> Newly executed (original or copy)b. <input type="checkbox"/> Copy from a prior application (37 CFR 1.63(d)) (for continuation/divisional with Box 17 completed)<ul style="list-style-type: none">i. <input type="checkbox"/> DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).</div> <div>6. <input type="checkbox"/> Application Data Sheet. See 37 CFR 1.76</div> <div>7. <input type="checkbox"/> CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)</div> <div>8. Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)<ul style="list-style-type: none">a. <input type="checkbox"/> Computer Readable Form (CRF)b. <input type="checkbox"/> Specification Sequence Listing on:<ul style="list-style-type: none">i. <input type="checkbox"/> CD ROM or CD-R (2 copies); orii. <input type="checkbox"/> paperc. <input type="checkbox"/> Statement verifying identity of above copies</div>				
Accompanying Application Parts				
9. <input checked="" type="checkbox"/> Assignment Papers (cover sheet & document(s))				
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11. <input type="checkbox"/> English Translation Document (if applicable)				
12. <input type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input type="checkbox"/> Copies of IDS Citations				
13. <input type="checkbox"/> Preliminary Amendment				
14. <input checked="" type="checkbox"/> Return Receipt Postcard (MPEP 503) (Should be specifically itemized)				
15. <input type="checkbox"/> Certified Copy of Priority Document(s) (if foreign priority is claimed)				
16. <input checked="" type="checkbox"/> Other <u>Check for filings fees in amount of \$2,156</u>				
17. If a CONTINUING APPLICATION, check appropriate box and supply the requisite information below and in a preliminary amendment, or in an Application Data Sheet under 37 CFR 1.76: <input type="checkbox"/> Continuation <input type="checkbox"/> Divisional <input checked="" type="checkbox"/> Continuation-in-part (CIP) of prior application No: <u>09/ 357,032</u> , filed <u>07/20/99</u> Prior application Information: Examiner <u>Not Assigned</u> Group/Art Unit <u>2836</u> For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 5b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation <u>can only</u> be relied upon when a portion has been inadvertently omitted from the submitted application parts.				
18. CORRESPONDENCE ADDRESS				
23.932				
<input type="checkbox"/> Customer Number or Bar Code Label (Insert Customer No. or Attach bar code label here) or <input type="checkbox"/> New correspondence address below				
NAME	Stephen G. Rudisill			
ADDRESS	Jenkins & Gilchrist 1445 Ross Avenue, Suite 3200			
CITY	Dallas	STATE	TX	ZIP CODE 75202-2799
COUNTRY	USA	TELEPHONE	312 425-8570	FAX 214 855-4300
Name (Print/Type)	Roger J. French		Registration No. (Attorney/Agent)	27,786
Signature			Date	10/23/00

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<h1 style="margin: 0;">FEE TRANSMITTAL</h1> <h2 style="margin: 0;">for FY 2000</h2> <p style="margin: 10px 0 0 40px;">Patent fees are subject to annual revision</p>		Complete if Known	
		Application Number	
		Filing Date	
		First Named Inventor	
		Examiner Name	
Group Art Unit			
Attorney Docket Number		47176-00538	
TOTAL AMOUNT OF PAYMENT		(\$) 2,156.00	

METHOD OF PAYMENT (check one)		FEE CALCULATION (continued)																																																																																																																																																																															
<p>1. <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge indicated fees and credit any over payments to:</p> <p>Deposit Account Number 10-0447/47176-00538</p> <p>Deposit Account Name Jenkins & Gilchrist</p> <p><input checked="" type="checkbox"/> Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17</p> <p><input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27</p> <p>2. <input checked="" type="checkbox"/> Payment Enclosed:</p> <p><input checked="" type="checkbox"/> Check <input type="checkbox"/> Credit card <input type="checkbox"/> Money Order <input type="checkbox"/> Other</p>		<p>3 ADDITIONAL FEES</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>Large Fee Code</th> <th>Entity Fee (\$)</th> <th>Small Fee Code</th> <th>Entity Fee (\$)</th> <th>Fee Description</th> <th>Fee Paid</th> </tr> </thead> <tbody> <tr><td>105</td><td>130</td><td>205</td><td>65</td><td>Surcharge - late filing fee or oath</td><td></td></tr> <tr><td>127</td><td>50</td><td>227</td><td>25</td><td>Surcharge - late provisional filing or cover sheet.</td><td></td></tr> <tr><td>139</td><td>130</td><td>139</td><td>130</td><td>Non-English specification</td><td></td></tr> <tr><td>147</td><td>2,520</td><td>147</td><td>2,520</td><td>For filing a request for reexamination</td><td></td></tr> <tr><td>112</td><td>920</td><td>112</td><td>920</td><td>Requesting publication of SIR prior to Examiner action</td><td></td></tr> <tr><td>113</td><td>1,840</td><td>113</td><td>1,840</td><td>Requesting publication of SIR after Examiner action</td><td></td></tr> <tr><td>115</td><td>110</td><td>215</td><td>55</td><td>Extension for response within first month</td><td></td></tr> <tr><td>116</td><td>390</td><td>216</td><td>195</td><td>Extension for response within second month</td><td></td></tr> <tr><td>117</td><td>890</td><td>217</td><td>445</td><td>Extension for response within third month</td><td></td></tr> <tr><td>118</td><td>1,390</td><td>218</td><td>695</td><td>Extension for response within fourth month</td><td></td></tr> <tr><td>119</td><td>310</td><td>219</td><td>155</td><td>Notice of Appeal</td><td></td></tr> <tr><td>120</td><td>310</td><td>220</td><td>155</td><td>Filing a brief in support of an appeal</td><td></td></tr> <tr><td>121</td><td>270</td><td>221</td><td>135</td><td>Request for oral hearing</td><td></td></tr> <tr><td>138</td><td>1,510</td><td>138</td><td>1,510</td><td>Petition to institute a public use proceeding</td><td></td></tr> <tr><td>140</td><td>110</td><td>240</td><td>55</td><td>Petition to revive unavoidably abandoned application</td><td></td></tr> <tr><td>141</td><td>1,240</td><td>241</td><td>620</td><td>Petition to revive unintentionally abandoned application</td><td></td></tr> <tr><td>142</td><td>1,240</td><td>242</td><td>620</td><td>Utility issue fee (or reissue)</td><td></td></tr> <tr><td>143</td><td>440</td><td>243</td><td>220</td><td>Design issue fee</td><td></td></tr> <tr><td>144</td><td>600</td><td>244</td><td>300</td><td>Plant issue fee</td><td></td></tr> <tr><td>122</td><td>130</td><td>122</td><td>130</td><td>Petitions to the Commissioner</td><td></td></tr> <tr><td>123</td><td>50</td><td>123</td><td>50</td><td>Petitions related to provisional applications</td><td></td></tr> <tr><td>126</td><td>240</td><td>126</td><td>240</td><td>Submission of Information Disclosure Stmt</td><td></td></tr> <tr><td>581</td><td>40</td><td>581</td><td>40</td><td>Recording each patent assignment per property (times number of properties)</td><td></td></tr> <tr><td>146</td><td>710</td><td>246</td><td>355</td><td>Filing a submission after final rejection (37 CFR 1.129(a))</td><td></td></tr> <tr><td>149</td><td>710</td><td>249</td><td>355</td><td>For each additional invention to be examined (37 CFR 1.129(b))</td><td></td></tr> <tr><td colspan="6">Other fee (specify) _____</td></tr> <tr><td colspan="6">Other fee (specify) _____</td></tr> <tr><td colspan="5" style="text-align: right;">SUBTOTAL (3)</td><td>(\$)</td></tr> </tbody> </table> <p>* Reduced by Basic Filing Fee Paid</p>		Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description	Fee Paid	105	130	205	65	Surcharge - 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SUBMITTED BY				Complete (if applicable)	
Typed or Printed Name	Roger J. French	Reg. Number	27,786	Telephone	(312) 425-8540
Signature		Date	OCTOBER 23, 2000		

Jenkins & Gilchrist
Customer No.: 23,932

PATENT
47176-00538

APPLICATION FOR UNITED STATES LETTERS PATENT

for

INTEGRATED REPEATER

by

Breck W. Lovinggood
Mano D. Judd
William P. Kuiper

006307 " 5246560

EXPRESS MAIL MAILING LABEL	
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<u>Corinne C. Roerpage</u>	
Signature	

INTEGRATED REPEATER**CROSS-REFERENCE TO RELATED APPLICATION**

This application is a continuation-in-part of the following prior U.S. patent
5 applications: Serial No. 09/357,032, filed July 20, 1999 entitled "Side-to-Side
Repeater"; Serial No. 09/483,234, filed January 14, 2000 entitled "Adaptive Cancellation
for Wireless Repeaters" and Serial No. 09/483,649, filed January 14, 2000 entitled "RF
Switched Beam Planar Antenna". The disclosures of these applications are incorporated
herein by reference.
10 U.S. patent application Serial No. 09/483,649 is a continuation-in-part of U.S.
patent application Serial No. 09/357,032, filed July 20, 1999.

FIELD OF THE INVENTION

This invention is directed to a repeater which is more "user friendly" than any
15 repeater on the market today, and, in addition, which offers a reduction in hardware,
installation, and alignment costs for a repeater site.

The invention is applicable to numerous markets where repeaters are used. Some
of these applications include Cellular, PCS (Personal Communication Services), 3rd
Generation PCS, WLL (Wireless Local Loop), SMR (Specialized Mobil Radio), MMDS
20 (Multipoint Multiuser Data Services), WLAN's (Wireless Local Area Network) at 900
MHz and 2400 MHz and Bluetooth (a new protocol/chip set that links user equipment,
being developed by Ericsson, IBM, Intel, Nokia and Toshiba).

BACKGROUND OF THE INVENTION

25 The use of repeaters by wireless service providers is increasing rapidly. Wireless
service providers are under constant pressure by their customers to improve and expand
coverage while decreasing the cost of service. This conflicts with the pressure from
investors and stockholders to increase earnings and decrease expenses. In addition, local
zoning and regulatory pressures often limit or preclude placement of base station sites in
30 the optimum locations. Because of these pressures, service providers require cost-
effective alternatives to installing additional base stations to expand coverage. The

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SUMMARY OF THE INVENTION

Briefly, in accordance with the foregoing, an integrated repeater comprises a housing having opposing sides, a donor antenna mounted on one of the opposing sides of

said housing; a null antenna mounted on the other of said opposing sides of said housing, repeater electronics mounted in said housing and operatively interconnecting said donor antenna and said null antenna, said donor antenna and said null antenna each comprising an antenna array having a plurality of antenna elements, and a beamforming arrangement
 5 for creating a desired antenna pattern of said donor antenna relative to a base station and a desired antenna pattern of said null antenna relative to subscriber equipment.

In accordance with another embodiment of the invention, an integrated repeater comprises a housing having opposing sides, a donor antenna mounted on one of the opposing sides of said housing; a null antenna mounted on the other of said opposing
 10 sides of said housing, repeater electronics mounted in said housing and operatively interconnecting said donor antenna and said null antenna, said donor antenna and said null antenna each comprising an antenna array having a plurality of antenna elements, and said repeater electronics including an interference cancellation circuit for cancelling interference feedback signals between said donor antenna and said null
 15 antenna in both an uplink path and a downlink path.

BRIEF DESCRIPTION OF THE DRAWINGS

In the drawings:

FIG. 1 is a simplified view of a repeater system of the prior art;

FIG. 2 is a simplified view, in a form similar to FIG. 1, showing a repeater in
 20 accordance with one embodiment of the invention;

FIG. 3 is a simplified isometric view of a modular repeater in accordance with one aspect of the invention;

FIG. 4 is a diagrammatic illustration of donor and null antenna steering in an integrated repeater system of the invention;

25 FIG. 5 is a block schematic diagram of one form of a repeater in accordance with the invention, utilizing a duplexed antenna;

FIG. 6 is a block schematic similar to FIG. 5 in illustrating implementation with separate transmit and receive antennas;

FIG. 7 is a simplified illustration of a patch antenna array;

30 FIG. 8 is a simplified representation, in a form similar to FIG. 7, of a dipole antenna array;

FIG. 9 is a flowchart or flow diagram of a repeater setup program in accordance with one embodiment of the invention; and

FIG. 10 is a flowchart or flow diagram of one embodiment of a repeater main operation loop.

5 FIG. 11 is a simplified block diagram of one signal path through a repeater system, as in FIGS. 5 and 6, showing the interference canceller as an adaptive cancellation circuit;

FIG. 12 is a block diagram (high level) of a (digitally) adaptive cancellation circuit in accordance with one embodiment of the invention;

10 FIG. 13 is a block diagram (high level) of the (digitally) adaptive cancellation circuit of FIG. 7 which shows the technique in further detail;

FIG. 14 is a block diagram of a repeater system/similar to the repeater system of claim 5, using the adaptive cancellation (AC) circuit of FIGS. 12 and 13;

15 FIGS. 15 and 16 show the directional characteristics of the AC blocks, for the downlink path (FIG. 15) and the uplink path (FIG. 16);

FIG. 17 shows a block diagram of the AC approach of the invention, applied to a repeater system similar to the repeater system of FIG. 6;

FIG. 18 is a block schematic diagram of a form of a repeater similar to that in FIG. 5, but without uplink and downlink channel modules;

20 FIG. 19 is a functional diagram showing beamsteering via a Butler matrix;

FIG. 20 is a simplified schematic diagram showing beamsteering using phase shifters;

FIG. 21 is a perspective view of a flat panel repeater design;

25 FIG. 22 is a perspective view illustrating a beamsteering scheme via tilting of flat panel arrays similar to the flat panel array of FIG. 21;

FIG. 23 and FIG. 24 show further embodiments of a flat panel array similar to that of FIG. 21.

FIG. 25 is a partial exploded perspective view of the flat panel array of FIG. 23 and FIG. 24, showing further details thereof;

30 FIG. 26 and FIG. 27 are side sectional and top views, respectively, of a reduced surface wave (RSW) patch antenna;

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A significant way of reducing installation cost is to reduce the amount of equipment that must be installed. A conceptual representation of the integrated repeater 150, as shown in FIG. 3, incorporates the donor antenna 122 and the null antenna 132 into sides of a repeater enclosure 152, such as by using conformal antennas. These antennas are relatively thin flat panels in FIG. 3. This arrangement reduces the installation cost for the separate donor and null antennas and the repeater electronics, and eliminates the coaxial cables shown in the repeater installation in FIG. 1. However, in order to optimally integrate the antennas into the repeater enclosure, several other aspects of this invention come into play.

Often a major challenge for wireless service providers is getting access to an equipment site situated in an optimum location. As shown in FIG. 4, the direction from the donor 122 and null 132 antennas of the repeater to the donor 24, and/or the null, site 36 (i.e., the base station and null fill area) is rarely at a right angle to the face of the donor or null antenna. In fact, this angle will vary significantly from site to site. To address this problem, one embodiment of the invention uses an antenna array with a beamformer network that can be programmed, e.g., from a look-up table. This allows the donor and null antennas to be beam-steered toward the location of the donor and null sites.

The antennas and beamformer can be implemented in several ways. The block diagrams in FIGS. 5 and 6 show two possible implementations. FIG. 5 is an implementation that uses a common antenna for transmit and receive for each of the donor and null antennas. FIG. 6 shows separate transmit and receive antennas 122a, 122b and 132a, 132b for each of the donor and null antennas. The implementation of FIG. 5 uses duplex filters or diplexers 160, 162 to separate the downlink path and uplink path signals for processing by the repeater. This approach has the advantage that the total area on the side of the integrated repeater 150 can be utilized to accommodate a larger antenna array. However, this approach requires that the filters 160, 162, provide all of the isolation between the downlink path and uplink path of the repeater. FIG. 6 is an implementation that uses separate transmit (132a, 132b) and receive (122a, 122b) antennas. This approach also has the advantage of using the isolation of the two antennas to reduce the filtering requirements in the repeater. However, this approach may increase the area required for the antennas on the sides of the repeater.

The remaining repeater circuits may be implemented in a number of ways. The embodiments shown in FIGS. 5 and 6 use a channel-selective approach. In this approach, a Low Noise Amplifier (LNA) 190 amplifies the low-level signal from each of the antennas, and is a very quiet amplifier to ensure that a good signal-to-noise ratio is maintained in the repeater. After the LNA 190, the desired signal moves to a channel module, comprising an uplink channel module 194 in the uplink path and a downlink channel module 196 in the downlink path. In the channel module, the desired signal is downconverted to a lower intermediate frequency and filtered to limit the spectrum amplified by the repeater to a single channel or set of channels. The intermediate frequency is then upconverted back to the original frequency of the desired signal. The output signal of the channel module is then routed to a power amplifier 198 in each path to create a high level transmit signal. In FIG. 6, respective filters 200, 202 are provided between the LNA's and Power Amplifiers and associated beamforming and selection circuits 204, 206 (to be described below) for the donor and null antennas.

The invention could use a direct RF, offset RF, or DSP, or GPS based repeater instead of the above-described channel-selective approach. Examples of DSP and GPS repeaters are shown respectively in copending U.S. patent application Serial No. 09/460,023, filed December 13, 1999 and Serial No. 09/513,543, filed February 25, 2000, which are incorporated herein by reference. A direct RF repeater performs all gain and filtering functions at the high frequency of the desired signal. An offset RF or frequency translating repeater is similar to a channel-selective repeater except that the upconversion of the intermediate frequency moves the signal to a new high frequency signal instead of the original frequency of the desired signal. This approach will minimize or eliminate the need for an interference canceller. A DSP repeater will still utilize an LNA and Power Amplifier but the processing functions in the channel module are handled by digitizing the desired signal, performing the function digitally and then converting the digital signal back to an analog signal.

As shown in FIGS. 7 and 8, the donor and null antennas can be implemented using antenna arrays 170, 180. Each antenna array uses a row of M horizontally spaced elements to achieve the desired azimuth beamwidth and N vertically spaced rows of elements to achieve the desired elevation beamwidth. This invention may use many different types of antenna elements in the antenna array. Some examples include patch

antennas 172 (FIG. 7) and bow tie dipoles 182 (FIG. 8). FIG. 7 is an example of a patch antenna array 170 with $M=4$ and $N=3$. FIG. 8 is an example of a bow tie dipole array 180 with $M=4$ and $N=2$.

The beamforming and selection networks 204, 206 and 204a, 204b, 206a, 206b in
 5 FIGS. 5 and 6, combine the antenna elements 172 or 182 in the antenna array 170 or 180
 with appropriate phase and amplitude to create the desired antenna pattern. Several
 methods can be used to implement the beamforming and selection networks. One
 method of implementing this is to use a $M \times N$ Butler matrix to perform the phasing and
 combining functions. The angle and elevation of each beam would be stored in a look-up
 10 table that the repeater uses to drive a diode or relay switch matrix to select the desired
 beam. This look-up table would be stored in a memory of the repeater controller 210 in
 order to map the desired azimuth and program other parameters and matrix settings for
 use by the Butler matrix.

The above-referenced co-pending application Serial No. 09/483,649, filed
15 January 14, 2000 describes implementations of beamforming for array antennas using a
Butler Matrix approach, which might be utilized in this regard.

One example of a Butler matrix for beamsteering is shown in FIG. 19. Here a plurality of antenna elements 600 are arranged in an array, in the illustrated embodiment comprising a 3 by 3 array. The antenna elements 600 may be patches 172 such as in FIG. 7, dipoles such as the dipoles 182 shown in FIG. 8 or another form of antenna elements. All of the antenna elements 600 in the array are coupled with a two-dimensional Butler matrix 602. The two-dimensional Butler matrix 602 is in turn coupled with an M:1 radio frequency (RF) switch 604. In the illustrated embodiment M=9, the total number of antennas 600 in the array coupled with the Butler matrix 602. The RF switch 604 is controlled by a controller or control circuit module 606 via a control output 605 which may also control other similar RF switches for the other repeater antenna array via an additional control output 607 as shown in FIG. 19. The controller 606 may be a part of the controller 210 of FIG. 5 or 6 or 18.

The controller 606 may be set to sequentially switch to the beams provided by the antenna 600 via the Butler matrix 602 and RF switch 604 to search for an optimal signal, such as the highest net power output. This is indicated in FIG. 19 by an RF connection 608 from the output of the switch 604 to an input of the control circuit 606 for

monitoring the RF output. Other parameters might be used to control switching such as the lowest noise or some other measure of signal quality. Thus, in operation, the control circuit 606 would switch antenna elements until an “optimum” signal output is located and then would remain connected to the antenna element at which the optimal signal was received. RF circuits similar to that shown in FIG. 14 are located between the switch 604 and a similar switch (not shown) which is coupled in the same fashion indicated to a similar Butler matrix to select a beam from a similar antenna array (not shown) at the opposite side of the repeater. The control module 606 similarly controls this second RF switch coupled with an antenna array at the other side of the repeater via a control line 607. The second RF switch may be controlled by the control circuit 606 on the same basis, for example, on the basis of signal strength or some other measure of signal quality. The RF circuit includes respective diplexers 609 (where the respective antenna elements 600 perform both transmit and receive functions) power amplifiers 610 and filters 612 similar to the circuit shown in FIG. 14.

A system of beamforming or beam selection other than a Butler matrix may also be utilized without departing from the invention. For example, the signal processor or controller 210 (e.g., in FIG. 5 or 6 or 18) in addition to its other functions could be programmed and adapted to perform a continuous variable, essentially linear beamforming function by continuous adjustment of the N beams coming in with a variable phase and amplitude weighting being applied, to develop a single beam direction to correspond to the desired beam direction of either the donor or null antennas for communicating with a base station or subscriber equipment. Various phase and amplitude settings could be prestored for a number of beams, for example N beams, each with a given directional characteristic or setting, from which the processor might choose the best match for a given situation.

Another method is to build the phasing and combining networks with variable phase devices in series with each antenna element. A look-up table of phase values for discrete angles and elevations can then be used to create the desired beam. In FIG 20, the antennas 600 in an N by N (e.g., 3 by 3) array are each coupled with a respective one of a plurality of phase shifters 620. The phase shifters are in turn coupled via a corporate feed 622 to an RF output A which may couple with the RF circuits as shown in FIG. 19. A

controller 624 is provided to control all of the phase shifters 620. The same arrangement would be utilized for the antenna on the opposite face of the repeater.

The latter arrangements would differ from the Butler matrix in that only one beam or directional output would be developed or generated for a given requirement or situation or relative location. In contrast, in the Butler matrix, a total of N beams would be available at all times with a switching network being utilized to select the one of these N beams best suited for a given situation or placement of the repeater tower relative to the base station and null fill area, respectively.

FIG. 21 illustrates a flat panel approach to repeater construction, as an alternative to the construction shown in FIG. 3. For relatively low power applications, a first face 122a may mount a plurality antenna elements 600 which may be patches, dipole or other antenna elements. Similar antenna elements may be mounted in an array on opposite face 132a. The relatively thin housing 152a may between the two faces or surfaces 122a and 132a may house the electronics.

Referring to FIG. 22 in an alternate design, each of a pair of flat panels 150b and 150c mount antenna elements (not shown) only on their outwardly facing surfaces 122b and 132b. The two panels 150b and 150c are pivotally mounted to a pair of brackets 650, 652 or other support structure at pivot points 654 and 656 and aligned pivot points (not shown) at the bottom edges of the respective panels 150b and 150c. A separate electronics housing or enclosure 152b may be provided coupled with the brackets or other support structure 650 and 652 intermediate the two flat panels 150b and 150c. The beamsteering may be accomplished by tilting (or rotating) the respective panels until the maximum signal strength, or some other measure of signal quality is achieved.

Referring to FIGS. 23 and 24, some further embodiments or modifications to the panels 122 and 132 are illustrated. In FIG. 23, each of the surfaces 122d and 132d may of antenna module 150d include a quantity of radio frequency absorbent material 670 generally surrounding the antenna elements 600.

FIG. 24 illustrates a flat panel antenna 150e of the type shown in FIG. 23 with the addition of a plurality of radio frequency chokes in the form of peripherally extending fins 680. These fins 680 may comprise relatively thin strips of conductive material located around the periphery of the antenna module 150d. In addition, quantities of radio

frequency absorbent material 685 may be located intermediate some or all of the fins about the peripheral surfaces of the module 150e.

Referring now to FIGS. 30-35, some alternate embodiments of RF chokes of various forms are illustrated. In FIGS. 30 and 31, the RF chokes 302 on an antenna 3150 are a series of concentric annular rings which extend generally orthogonally from the antenna face 3202, in contrast to the radially extending annular rings 680 shown in the embodiment of FIG. 24. Further, details of the annular ring structure are shown in the side sectional view of FIG. 31. In the side sectional view of FIG. 31, the choke ring structure 3002 resembles a corrugated structure.

Similarly, FIGS. 32 and 33 show an antenna 3250 having circumferentially spaced, congruent chokes 3202. The chokes 3202 in the embodiment of FIGS. 32 and 33 are generally rectangular or rectilinear in configuration and extend orthogonally outwardly from opposite sides of central flat surfaces 3302, 3304 which may mount respective radiating elements of a repeater structure. FIG. 33 is an enlarged partial sectional view illustrating further details of the chokes 3202 and the surfaces 3302, 3303.

FIGS. 34 and 35 illustrate a structure essentially similar to that shown in FIGS. 32 and 33, however, in a circular configuration. Thus, like reference numerals, with prefixes 34 and 35, are utilized to designate like elements and components.

Referring to FIG. 25, respective uplink and downlink patch elements on each side of an antenna may be orthogonally polarized. This is illustrated in FIG. 25, wherein patch elements on faces 122f and 132f are orthogonally polarized with respect to the corresponding patch elements on the other surface. For example, the transmit/downlink radiating element 250 on the surface 122f is polarized orthogonally to the transmit downlink radiating element 250 on the opposite surface 122f. Similarly, the uplink/receive patch 256 on the surface 122f is orthogonally polarized relative to the uplink/receive patch 258 on the surface 132f.

An antenna is (simplifying somewhat) a path by which electrons get accelerated back and forth (i.e. a "race-track"). For example, in a dipole antenna, electrons accelerate from one end, towards the center (where they have the greatest velocity), then de-accelerate towards the other end (where the velocity is the slowest). They then turn around and accelerate back the other way. They do this at the rate of the resonant frequency of the antenna. The feed point of the antenna (for a dipole, at the center) is the

position in which the electrons are moving the fastest. Thus, voltage (potential) of the antenna is tapped from this position. Electromagnetic energy therefore radiates from the ends of an antenna (dipole, or patch) in the direction of the accelerating electrons. This direction is called the antenna polarization (direction). Displacement currents (virtual
 5 electrons) therefore go from one end of the dipole, curve out in space, and terminate at the other end of the dipole. For two adjacent antennas, oriented in the same direction, where one is transmitting (active) and the other is receiving (passive), the active antenna pushes virtual electrons into space which terminate on the passive antenna. These virtual electrons then force the actual electrons on the surface of the passive antenna to
 10 accelerate, and induce a potential at its feed point. However, if the two antennas were not oriented in the same direction (being orthogonal....or perpendicular; for instance, as described above in FIG. 25) then the active antenna could not accelerate electrons on the other (passive) antenna. The "race-track" on the passive antenna is extremely short. These antennas are considered orthogonal, and therefore do not couple. Orthogonal
 15 antennas, on opposite sides of the repeater, would not couple and therefore would appear isolated from each other. Thus, the system gain could be increased without inducing ringing.

In an alternative embodiment, a reduced surface wave (RSW) type of antenna structure might be utilized in place of the patches shown in the prior figures of drawing.
 20 FIGS. 26 and 27 are a side sectional view and a top plan view of one example of a probe-fed, shorted annular ring, reduced surface wave patch antenna 260. An RSW patch antenna element, is simply a patch that focuses more energy in the directed area, and not to the sides; near the ground plane. There are many types of RSW patches, but the most common is a recessed patch inside a partial cavity. The cavity walls act as a field
 25 suppressor, and "catch" field lines that are directed to the sides of the patch, rather towards the direction perpendicular to the patch and ground plane. If both patches (on opposite sides) are RSW patches, then they would have reduced coupling (i.e. greater isolation); which allows the system active gain to be increased.

RSW microstrip antennas produce only a small amount of surface-wave radiation.
 30 In addition, if printed on electrically thin substrates, these antennas only weakly excite lateral waves (space waves that propagate horizontally along the substrate interface). As a result, these antennas do not suffer from the deleterious effects of surface and lateral

wave scattering. These characteristics make the RSW antenna ideal for applications where the supporting substrate or ground plane of the antenna is small, in which case diffraction of the surface and lateral waves from the edges of the structure may be quite significant for conventional microstrip patch antennas. RSW antennas also have promise
 5 for array applications, where the presence of surface and lateral waves for conventional patch radiators produce significant mutual coupling and may lead to scan blindness.

For a given size antenna element (patch, dipole,) increasing the size of the ground plane behind the element reduces the Front to Back (F/B) ratio of the antenna. More specifically, the larger the ground plane, the less energy gets radiated to the back
 10 side. Thus, increasing the size (of the faces) of the side-to-side repeater reduces the amount of energy that each face radiates to the backward face. Another way of explaining this is that by increasing the size of the repeater, the lower the coupling between the two opposite antennas (i.e. patches). This therefore increases the isolation between the patches on opposites sides, and allows one to increase the active gain for the
 15 system. However, where the size of the ground plane is limited by other considerations, the RSW patch technology may be employed.

To date, the most promising RSW design is the Shorted-Annular-Ring Reduced-Surface-Wave (SAR-RSW) antenna. One example of this type of antenna 260, shown in FIGS. 26 and 27, is a conventional annular ring microstrip antenna 262 with an inner
 20 boundary 264 short-circuited to a conducting ground plane 266. The outer radius dimension is chosen to eliminate surface-wave excitation from the equivalent ring of magnetic current at the outer edge of the antenna that corresponds to the TM_{011} cavity patch mode. (The modes are denoted using the notation $TM_{\phi p}$.) The inner radius is chosen to make the patch resonant at the design frequency.

FIGS. 28 and 29 are simplified diagrams illustrating beamsteering via the use of
 25 various delay lengths by using striplines of different lengths on different layers of a multi-layer printed circuit board selectable via RF switch (FIG. 28) or striplines of different lengths printed on the same circuit board and selectable via RF switches (FIG. 29). Thus, in FIG. 28, antenna elements 280, 282 and 284, 286 are each coupled to
 30 multiple striplines of different lengths, represented by various solid and broken lines designated generally by the reference numeral 288. All of these lines 288 of various lengths are coupled together at radio frequency (RF) summers 281. That is, all of the

lines 288 of a first length are coupled to one terminal a summer 283. All of the lines 288 of a second length are coupled to a summer 285, and so forth. A radio frequency (RF) switch 287 operated in response to a control signal on a control line 289 selects from among the lines of different lengths to the various antenna elements 280, etc. attached to the summers 283, 285, etc. The control signal may be produced automatically in response to a measurement of signal strength or some other optimal signal quality, in order to accomplish beamsteering via the selection or adjustment of stripline length.

In the approach shown in FIG. 29, the selection of striplines 290 of varying length is accomplished at the antenna elements 292, 294, 296, etc. by respective radio frequency switches 291, 293 and 295. All of these delay lines of varying length feed a common RF output 297. The delay line length for each antenna may be selected either independently or in unison with the selection of delay lines other antennas, in response to suitable control signals (C) in much the same fashion as in FIG. 28, and/or as described above in response to detection of an optimal signal level or some other optimal signal quality measurement.

Typically, a repeater site uses the physical separation of the donor and null antennas to achieve enough isolation to allow the repeater to operate with gains of 60 to 95 dB. Because the antennas are located relatively close together in the integrated repeater of FIG. 3 (or FIG. 21 or FIG. 23), another approach is needed to achieve isolation. Such an approach can include the use of radio frequency chokes in the enclosure between the antennas to reduce the coupling between the antennas, or the use of an adaptive interference canceller to provide additional gain and phase margin, is described in the above-referenced application Serial No. 09/483,234, filed January 14, 2000. In this latter copending application, an adaptive interference canceller samples the signal at the output of the repeater and compares it to the signal at the input of the repeater. It then takes the undesired signal and injects a version of it into the input of the repeater with suitable phase and amplitude to cancel the undesired signal in the output. The integrated repeater of the present invention may use such an interference canceller 208 in the downlink and/or the uplink paths as required for a specific application.

In this regard, a limiting characteristic for repeaters is that of the feedback loop, or conversely, the isolation between the two opposing antennas (or sensors). That is, the total front to back (F/B) ratio for the system, or isolation, must be higher than the desired

gain. Generally speaking, the isolation between donor and null antennas is equal to the total repeater gain plus some margin, typically around 10 to 15 dB. Therefore, the repeater gain will in general be less than the isolation minus the margin. For example, if the isolation between antennas is around 60 dB, then the maximum repeater gain allowed will be about 45 dB. For PCS frequencies, these figures may result in a repeater range of less than 100 feet.

In a scattering environment, which is common in PCS, every 6 dB of additional system gain will double the coverage distance. Thus, obtaining an additional 24 dB of isolation between the two antennas, will allow the range to double 4 times, to 1600 feet. For conventional repeater systems as in FIG. 1, where the two antennas and repeater electronics are in three separate enclosures, and locations, the donor antenna (to the base station) and null antenna (to the desired coverage area), are separated in space by (usually) more than 10 feet. This distance adds over 50 dB to the isolation between antennas, generating a total isolation value of well over 100 dB. Therefore, with a 15 dB margin, this type of system can utilize a total gain of up to 85 dB or more, which results in fairly large range and coverage.

For the integrated repeater of this invention, where the opposing antennas are in or on the same housing or enclosure, and separated in space by as little as a few inches, isolation is typically limited to a value below 80 dB or so. This therefore allows a total repeater gain of no more than 65 dB, which may limit the system range to a few hundred feet or less.

The below-described embodiment of an adaptive cancellation approach removes a significant portion (between 10 dB and 40 dB) of the feedback signal power, therefore increasing the total system isolation by the same amount (10 to 40 dB). This additional isolation can be used to achieve greater repeater gain, and therefore significantly extend the range of the system. This is especially useful in the integrated repeater described in this application. The cancellation scheme (one embodiment of which is described in further detail below) uses digitally processed information to generate a signal, which, when added to the original input signal, cancels the feedback signal.

Finally, the microprocessor or controller (repeater controller) 210 provides the repeater control functions. This controller provides all setup, communications, and monitoring functions for the repeater. These functions include those related to setting the

beamforming and selection functions to the desired beam, as mentioned above, and setting the amplifier gain and frequency of operation to the maximum usable gain for stability and the power rating of the repeater.

The controller's functions may also include monitoring of power levels at various points in the system, monitoring of the status of the devices in the system, for example for over-power, under-power, oscillation, etc. The controller may also include communication ports for communication with outside devices. For example, a local connection, such as an RS-232 port may be provided to communicate with a laptop computer which may be used in the field to exchange data with the controller, update data or program routines, or the like. A remote communication port or protocol may also be employed to enable communications with a network management system, through a local telephone company or wireless serial communication port, such as a data modem or TCP/IP (Transmission Control Protocol/Internet Protocol) or SNMP(Simple Network Management Protocol). In this regard the controller 210 may comprise a microprocessor with a UART (Universal Asynchronous Receive Transmit) to enable the desired communications and command structures and protocols.

FIGS. 9a and 9b are a sample flow chart for the initialization of the repeater, which includes beam selection on the donor antenna and null antenna and the initial gain settings. The blocks in the flowchart of FIG. 9 are as follows:

<u>Reference No.</u>	<u>Function</u>
300	Power On/Reset
302	Disable Repeater
304	Get Donor Site Coordinates & Elevation
306	Get Null Area Coordinates & Elevation
308	Get Repeater Site Coordinates & Elevation
310	Get Compass Direction of Repeater
312	Calculate Pointing Angle to Donor
314	Get Donor Beamformer Settings From Look-Up Table
316	Set Donor Beamformer
318	Calculate Pointing Angle to Null
320	Get Null Beamformer Settings from Look-Up Table
322	Set Null Beamformer

324	Set Repeater Gain to Minimum
326	Set Repeater Channel Frequency
328	Enable Repeater
330	Measure Forward Power
332	Forward Power Over Limit?
334	Display Warning
336	Disable Repeater
338	Increase Gain 6 dB
340	Measure Forward Power
342	Forward Power Over Limit?
344	Forward Power Increase 6 dB?
346	Decrease Gain 6 dB
348	Decrease Gain 6 dB
350	Go to Main Loop
352	End

FIG. 10 is a sample flow chart for a main operational loop of the repeater control program, and includes only the functions related to auto gain control of the repeater. The blocks in the flow chart of FIG. 10 are as follows:

<u>Reference No.</u>	<u>Function</u>
400	Main Loop
402	Wait Time T1
404	Measure Forward Power
406	Forward Power Over Limit?
408	Decrease Gain 2 dB
410	Increase Gain 2 dB
412	Measure Forward Power
414	Forward Power Increase 2 dB
416	Decrease Gain 2 dB
418	Decrease Gain 4 dB

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Referring now to FIGS. 11-18, an embodiment of an adaptive cancellation system will be described. FIG. 11 shows a simplified block diagram of one path through a repeater

system. The input signal, $S(t)$ from the base station (for the downlink path) (would be reversed for the signal for the uplink path), is received via an antenna 122, bandpass filtered, amplified (at 528, with active gain = G), filtered again, and finally transmitted by an antenna 132. Some of the transmitted signal energy 527 couples back (through space, or through the electronics) into the receive antenna 122. This is denoted as the feedback signal, $f(t)$, which is simply a delayed version (attenuated) of the original signal, $S(t)$. Therefore, the composite signal, $S(t) + f(t)$, is fed into the amplifier A (528), with output $G(S(t) + f(t))$. Assume for example, that the antennas have 0 dBi gain, then the new feedback signal is $G f(t)$. The propagation of this signal, back to the input antenna, will incur attenuation, H . Therefore, the amplified, attenuated signal at the input antenna will be $H G f(t)$. If this signal is comparable in power to the original signal $S(t)$, then the amplifier 28 will go unstable, and oscillate (ring). This oscillation will cause severe distortion in the desired signal.

FIG. 11 also shows an adaptive cancellation circuit 540. The goal of this circuit 540 is to create a 180 degree shifted $f(t)$ signal $-f(t)$, and sum it with the input signal (which includes $f(t)$), at a summing junction 545, and thereby remove the feedback signal $f(t)$.

FIG. 12 shows a general block diagram (high level) of one form of the adaptive cancellation circuit 540. In this approach, the input (RF) signal is summed at the junction 545 with a modulated signal constructed via a digitally adaptive process, to destructively interfere with the feedback signal embedded in the input composite signal. After the summation, the composite signal, $S(t) + f(t)$, is digitally sampled and digitally processed via a digital signal processor (DSP) 550, which computes an intermediate signal for a modulator 552. The modulator 552 takes the intermediate signal, and a sample of the amplified (output) signal, and creates a phase shifted copy $-f(t)$ of the interfering signal $f(t)$. This process will work with many, if not most, of the digitally adaptive algorithms for feedback control. Additionally, this methodology does not require an injected signal (training or pilot tone, or wideband noise), for the adaptive process. The entire circuit shown in FIG. 12 may be designated as an "AC block" 555.

FIG. 13 shows the circuit 540 in further detail. The DSP 550 is a combination of an RF downconverter 560 to shift the signal to an intermediate frequency that allows digital sampling, an analog-to-digital (A/D) converter 562 which digitizes the analog

signal, and a processor 564 which performs the required operations to compute the intermediate signal. The modulator 552 is a combination of a controllable attenuator 566, and an I/Q modulator 568. Additional details shown in FIG. 13 include respective couplers 572 and 574 which couple the signals from the signal path to and from the adaptive cancellation circuit 540, a first coupler 572 being interposed between the summation junction 545 and the filter 200 and the second coupler 574 being at the output of the power amplifier 528. In addition to the couplers 572 and 574 respective delay lines 582 and 584 may be employed at either end of the RF path, one just prior to the summing junction 545 and one subsequent to the coupler 574.

FIG. 14 shows a block diagram of a repeater system similar to that of FIG. 5 using adaptive cancellation (AC) circuit blocks 555, further details of which are shown in FIGS. 15 and 16, as well as in FIGS. 12 and 13. The "direction" of the circuit (AC block) 555 in each RF path has been taken into account. In this system, each (uplink, downlink) path has a separate AC circuit block 555. In FIG. 5, the function of the filters 200, 202 is incorporated in the diplexers 160, 162; in FIGS. 11-18, they are shown separately.

FIGS. 15 and 16 show the directional characteristics of the AC circuit blocks 555, whether for the downlink or uplink path. The blocks are "mirror images" of one another, differing by the direction of the desired signal, with the arrows 575 denoting the directionality of each circuit 555, in FIGS. 14, 15 and 16.

FIGS. 14 and 18 show block diagrams of the adaptive cancellation (AC) approach, applied to repeater systems similar to the ones shown and described in FIGS. 5 and 6. As indicated above, FIGS. 14 and 17 show the case for a single antenna unit on each side, including frequency diplexers to separate each path (or frequency band), while FIG. 18 shows the approach when using separate Tx and Rx antennas.

FIG. 36 shows a solar panel 800 with a battery 802 on a relatively thin, flat antenna 804 of the type described above. The addition of a "solar panel with battery" system would allow the repeater to be installed in a location with sunlight, and therefore mitigate the requirements for an external (DC) power source. The system would actually operate from the battery unit, which would occasionally (when the sun is up) get re-charged from the embedded solar panel unit. This would be an excellent application for spot coverage requirements for a repeater, where there is currently not a local power

5 The Battery system would be inside the unit; adjacent to the RF hardware (amplifiers, etc.).

10 modifications, changes, and variations may be apparent from the foregoing descriptions
without departing from the spirit and scope of the invention as defined in the appended
claims.

WHAT IS CLAIMED IS:

1. An integrated repeater comprising:
a housing having opposing sides;
a donor antenna mounted closely adjacent to one of the opposing sides of said
5 housing;
a null antenna mounted closely adjacent to the other of said opposing sides of said
housing;
repeater electronics mounted in said housing and operatively interconnecting said
donor antenna and said null antenna; and
10 a beamforming arrangement for creating a desired antenna pattern of said donor
antenna and a desired antenna pattern of said null antenna.
2. The integrated repeater of claim 1 wherein said repeater electronics
further include a downlink channel module and an uplink channel module operatively
15 coupled between said donor and null antennas.
3. The integrated repeater of claim 1 wherein said donor antenna and said
null antenna each comprise an antenna array having a plurality of antenna elements and
wherein said beamforming arrangement includes a Butler matrix comprising a part of
20 said repeater electronics and operatively coupled with each of said donor antenna array
and said null antenna array.
4. The integrated repeater of claim 1 wherein said donor antenna and said
null antenna each include separate transmit and receive antenna arrays.
25
5. The integrated repeater of claim 1 wherein each of said donor and null
antennas comprise an antenna array comprising a plurality of patch antenna elements
arranged in an M by N array.
6. The integrated repeater of claim 5 wherein said donor antenna and said
30 null antenna each include separate transmit and receive antenna arrays.

7. The integrated repeater of claim 5 wherein each of said patch antenna elements comprises a reduced surface wave element.

8. The integrated repeater of claim 1 wherein said donor antenna and said null antenna each comprise an antenna array having a plurality of antenna elements and wherein said beamforming arrangement includes a plurality of stripline feeds of varying lengths coupled with said antenna elements and a switching circuit for selecting one or more of said striplines to achieve a desired stripline delay.

9. The integrated repeater of claim 1 wherein said repeater electronics further include an interference cancellation circuit for substantially cancelling radio frequency interference feedback signals between said donor and null antennas in both an uplink path and a downlink path.

10. The integrated repeater of claim 1 wherein said repeater electronics further include a controller for providing setup, communications and monitoring functions for the repeater.

11. The integrated repeater of claim 9 wherein said repeater electronics further include a controller for providing setup, communications and monitoring functions for the repeater.

12. The integrated repeater of claim 1 wherein said donor antenna and said null antenna each comprise an antenna array having a plurality of antenna elements, wherein said beamforming arrangement comprises a Butler matrix comprising a part of said repeater electronics and operatively coupled with each of said donor antenna and said null antenna, and further including a memory for storing angle and elevation information for use in operating said Butler matrix.

13. The integrated repeater of claim 12 wherein said repeater electronics include amplifiers coupled intermediate said donor antenna and said null antenna and

further including a controller for setting an amplifier gain of said amplifiers, using information stored in said memory.

14. The integrated repeater of claim 9 wherein said beamforming arrangement
5 comprises a Butler matrix comprising a part of said repeater electronics and operatively coupled with each of said donor antenna and said null antenna and wherein said repeater electronics further include a memory for storing angle and elevation information for use in operating said Butler matrix.

10 15. The integrated repeater of claim 14 wherein said repeater electronics include amplifiers coupled intermediate said donor antenna and said null antenna and a controller which uses said memory for setting an amplifier gain of said amplifiers.

16. The integrated repeater of claim 1 wherein said donor antenna and said
15 null antenna each comprise an antenna array having a plurality of antenna elements and wherein said beamforming arrangement includes a plurality of phase shifters respectively coupled with the antenna elements and each of said donor antenna array and said null antenna array, a controller for controlling operation of said phase shifting elements, and said phase shifting elements being coupled with a corporate feed to a radio frequency
20 output.

17. The integrated repeater of claim 1 wherein said donor antenna and said
null antenna each comprise an antenna array having a plurality of antenna elements and wherein said beamforming arrangement comprises a plurality of striplines, each having a
25 length adjusted for variable stripline delay, coupled respectively with the antennas elements of the donor antenna array and null antenna array and a plurality of RF switches coupled with said striplines.

18. The integrated repeater of claim 1 wherein said donor antenna and said
30 null antenna are mounted upon said opposing sides of said housing.

19. The integrated repeater of claim 1 wherein said donor antenna and said null antenna each comprise a flat panel and said beamforming arrangement comprising a mounting structure for mounting each of said flat panels on one side of said housing for rotational movement relative to said housing.

5

20. An integrated repeater comprising:
a housing having opposing sides;
a donor antenna mounted closely adjacent to one of the opposing sides of said housing;

10 a null antenna mounted closely adjacent to the other of said opposing sides of said housing;

repeater electronics mounted in said housing and operatively interconnecting said donor antenna and said null antenna; and

15 said repeater electronics including an interference cancellation circuit for cancelling interference feedback signals between said donor antenna and said null antenna in both an uplink path and a downlink path.

21. The integrated repeater of claim 20 wherein said repeater electronics further include a downlink channel module and an uplink channel module operatively
20 coupled between said donor and null antennas.

22. The integrated repeater of claim 20 wherein said donor antenna and said null antenna each include separate transmit and receive antenna arrays.

25 23. The integrated repeater of claim 20 wherein each of said donor and null antennas comprise an antenna array comprising a plurality of patch antenna elements arranged in an M by N array.

24. The integrated repeater of claim 23 wherein said donor antenna and said
30 null antenna each include separate transmit and receive antenna arrays.

25. The integrated repeater of claim 23 wherein each of said patch antenna elements comprises a reduced surface wave element.

26. The integrated repeater of claim 20 wherein said donor antenna and said null antenna each comprise an antenna array having a plurality of antenna elements and wherein said beamforming arrangement includes a plurality of stripline feeds of varying lengths coupled with said antenna elements and a switching circuit for selecting said striplines to achieve a desired stripline delay.

27. The integrated repeater of claim 20 and further including a means for creating a desired antenna pattern of said donor antenna relative to a base station and a desired antenna pattern of said null antenna relative to subscriber equipment.

28. The integrated repeater of claim 27 wherein said donor antenna and said null antenna each comprise an antenna array having a plurality of antenna elements, and wherein said beamforming means includes a Butler matrix comprising part of said repeater electronics and operatively coupled with each of said donor antenna array and said null antenna array.

29. The integrated repeater of claim 20 wherein said repeater electronics further include a controller for providing setup, communications and monitoring functions for the repeater.

30. The integrated repeater of claim 27 wherein said repeater electronics further include a controller for providing setup, communications and monitoring functions for the repeater.

31. The integrated repeater of claim 30 wherein said donor antenna and said null antenna each comprise an antenna array having a plurality of antenna elements, wherein said beamforming means comprises a Butler matrix comprising part of said repeater electronics and operatively coupled with each of said donor antenna array and

said null antenna array, and further including a memory for storing angle and elevation information for use in operating said Butler matrix.

32. The integrated repeater of claim 31 wherein said repeater electronics
5 include amplifiers coupled intermediate said donor antenna and said null antenna and further including a controller for setting an amplifier gain of said amplifiers, using information stored in said memory.

33. The integrated repeater of claim 20 wherein said interference cancellation
10 circuit comprises an adaptive cancellation circuit which generates a cancellation signal, which when added to a radio frequency signal substantially cancels any feedback signal present in said radio frequency signal.

34. The integrated repeater of claim 33 wherein said adaptive cancellation
15 circuit comprises a digital signal processor circuit which receives an incoming radio frequency signal having a feedback signal component, and which digitally samples and processes said incoming radio frequency signal to generate an intermediate frequency signal, and a modulator circuit which receives said intermediate frequency signal and a sample of a radio frequency output signal and generates said cancellation signal.

20 35. The integrated repeater of claim 34 wherein said digital signal processor comprises a radio frequency downconverter which converts said incoming radio frequency signal to a lower frequency signal for digital sampling, an analog-to-digital converter coupled to the radio frequency downconverter, which analog-to-digital
25 converter digitizes said lower frequency signal, and a processor coupled to the analog-to-digital converter which computes a desired intermediate frequency signal for the modulator.

36. The integrated repeater of claim 34 wherein said adaptive cancellation
30 circuit further includes a summing junction which receives and sums said intermediate frequency signal and said incoming radio frequency signal.

37. The integrated repeater of claim 36 wherein said digital signal processor circuit receives an output of said summing junction.

38. The integrated repeater of claim 35 wherein said modulator circuit
5 comprises a controllable attenuator which receives and attenuates the radio frequency output signal and an I/Q modulator coupled to said attenuator and to said processor.

39. The integrated repeater of claim 27 wherein said donor antenna and said null antenna each comprise an antenna array having a plurality of antenna elements and
10 wherein said beamforming means includes a plurality of phase shifters respectively coupled with the antenna elements in each of said donor antenna array and said null antenna array, a controller for controlling operation of said phase shifting elements, and said phase shifting elements being coupled with a corporate feed to a radio frequency output.

15

40. The integrated repeater of claim 27 wherein said donor antenna and said null antenna each comprise an antenna array having a plurality of antenna elements and wherein said beamforming means comprises a plurality of striplines having length
adjusted for variable stripline delay and coupled respectively with the antennas elements
20 of the donor antenna array and null antenna array and a plurality of RF switches coupled with said striplines.

41. The integrated repeater of claim 27 wherein said donor antenna and said null antenna are mounted upon said opposing sides of said housing.

25

42. The integrated repeater of claim 27 wherein each of said donor antenna and said null antenna each comprise flat panels and wherein said beamforming means comprises a mounting structure for mounting each of said flat panels on one side of said housing.

30

43. The integrated repeater of claim 1 wherein said donor antenna and said null antenna each comprise a relatively flat antenna face having one or more antenna

elements mounted thereon and a quantity of radio frequency absorbent material on each said antenna face.

44. The integrated repeater of claim 1 wherein said donor antenna and said
5 null antenna each comprise a relatively flat antenna face having one or more antenna
elements mounted thereon and a plurality of radio frequency chokes surrounding said
antenna face.

45. The integrated repeater of claim 1 wherein said donor antenna and said
10 null antenna each comprise a relatively flat antenna face having one or more antenna
elements mounted thereon each of said antennas defining a peripheral edge surface, and a
plurality of radio frequency chokes surrounding said peripheral edge surface.

46. The integrated repeater of claim 45 and further including a quantity of
15 radio frequency absorbent material between at least some of said chokes.

47. The integrated repeater of claim 20 wherein said donor antenna and said
null antenna each comprise a relatively flat antenna face having one or more antenna
elements mounted thereon and a quantity of radio frequency absorbent material surround
20 each said antenna face.

48. The integrated repeater of claim 20 wherein said donor antenna and said
null antenna each comprise a relatively flat antenna face having one or more antenna
elements mounted thereon and a plurality of radio frequency chokes surrounding said
25 antenna face.

49. The integrated repeater of claim 20 wherein said donor antenna and said
null antenna each comprise a relatively flat antenna face having one or more antenna
elements mounted thereon, and a peripheral edge surface, and a plurality of radio
30 frequency chokes surrounding said peripheral edge surface.

50. The integrated repeater of claim 49 and further including a quantity of radio frequency absorbent material between at least some of said chokes.

51. The integrated repeater of claim 45 and further including a quantity of radio frequency absorbent material on each said antenna face.

52. The integrated repeater of claim 47 and further including a plurality of radio frequency chokes surrounding said antenna face.

53. The integrated repeater of claim 19 wherein said donor antenna and said null antenna each comprise a relatively flat antenna face having one or more antenna elements mounted thereon and a peripheral edge surface, and a plurality of radio frequency chokes surrounding said peripheral edge surface.

54. The integrated repeater of claim 53 and further including a quantity of radio frequency absorbent material between at least some of said chokes and on each said antenna face.

55. An integrated repeater comprising:
a housing having opposing sides;
a donor antenna mounted on one of the opposing sides of said housing;
a null antenna mounted on the other of said opposing sides of said housing;
repeater electronics mounted in said housing and operatively interconnecting said donor antenna and said null antenna;

wherein said donor antenna and said null antenna each comprise a relatively flat antenna face having one or more antenna elements mounted thereon and a quantity of radio frequency absorbent material on each said antenna face.

56. The integrated repeater of claim 1 wherein said donor antenna and said null antenna each comprise a relatively flat antenna face having one or more antenna elements mounted thereon and a quantity of radio frequency absorbent material on each said antenna face.

57. The integrated repeater of claim 1 wherein said donor antenna and said null antenna each comprise a relatively flat antenna face having one or more antenna elements mounted thereon and a plurality of radio frequency chokes surrounding said antenna face.

58. The integrated repeater of claim 1 wherein said donor antenna and said null antenna each comprise a relatively flat antenna face having one or more antenna elements mounted thereon and a peripheral edge surface, and a plurality of radio frequency chokes surrounding said peripheral edge surface.

59. The integrated repeater of claim 45 and further including a quantity of radio frequency absorbent material between said chokes.

60. The integrated repeater of claim 55 wherein each of said donor antenna and said null antenna each comprise flat panels and further including a beamforming arrangement comprising a mounting structure for mounting each of said flat panels on one side of said housing for rotational movement relative to said housing.

61. The integrated repeater of claim 55 and further including a plurality of radio frequency chokes surrounding said antenna face.

62. The integrated repeater of claim 61 and further including a quantity of radio frequency absorbent material between at least some of said chokes.

63. A method of repeating a radio frequency signal comprising:
 receiving said radio frequency signal at one of a donor antenna mounted on one of opposing sides of a housing and a null antenna mounted on the other of said opposing sides of said housing;
 routing said signal through repeater electronics mounted in said housing and operatively interconnecting said donor antenna and said null antenna;

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71. The method of claim 63 wherein said beamforming includes phase shifting antenna elements in a donor antenna array and a null antenna array using a controller and coupling phase shifted signals via a corporate feed to a radio frequency output.

72. The method of claim 63 wherein said beamforming comprises selecting the lengths of a plurality of striplines coupled respectively with the antennas elements of the donor antenna array and null antenna array for variable stripline delay and RF switching said striplines.

5

73. The method of claim 63 wherein said beamforming comprises rotating said donor antenna and said null antenna.

74. A method of repeating a radio frequency signal comprising:
10 receiving said radio frequency signal at one of a donor antenna mounted on one of opposing sides of a housing and a null antenna mounted on the other of said opposing sides of said housing;
routing said signal through repeater electronics mounted in said housing
operatively interconnecting said donor antenna and said null antenna;
15 transmitting said radio frequency signal from the other of said donor and null antennas; and
cancelling interference feedback signals between said donor antenna and said null antenna in both an uplink path and a downlink path.

20 75. The method of claim 74 and further including beamforming for creating a desired antenna pattern of said donor antenna and a desired antenna pattern of said null antenna.

76. The method of claim 74 wherein said cancelling interference feedback
25 signals comprises generating a cancellation signal, which when added to a radio frequency signal substantially cancels any feedback signal present in said radio frequency signal.

77. The method of claim 76 wherein said generating comprises digitally
30 sampling and processing an incoming radio frequency signal having a feedback signal component to generate an intermediate frequency signal, and processing said

78. The method of claim 77 wherein said processing comprises converting
5 said incoming radio frequency signal to a lower frequency signal for digital sampling,
digitizing said lower frequency signal, and computing a desired intermediate frequency
signal for the modulator.

80. The method of claim 77 wherein said processing comprises controllably attenuating the radio frequency output signal sample and I/Q modulating the attenuated signal sample.

82. The integrated repeater of claim 20 wherein the donor antenna and null
20 antennas are orthogonally polarized.

25 84. The integrated repeater of claim 1 and further including a solar powered
battery mounted to said housing.

30

86. The integrated repeater of claim 55 and further including a solar powered battery mounted to said housing.

5 housing;

housing;

donor antenna and said null antenna; and

10 a solar powered battery mounted to said housing.

An integrated repeater includes a housing having opposing sides, a donor antenna mounted on one of the opposing sides, a null antenna mounted on the other of the opposing sides, and repeater electronics mounted in the housing and operatively interconnecting the donor antenna and the null antenna. The repeater electronics include a beamforming arrangement for creating a desired antenna pattern of the donor antenna relative to a base station and a desired antenna pattern of the null antenna relative to subscriber equipment, and interference cancellation electronics for cancelling interference feedback signals between the donor antenna and the null antenna in both an uplink path and a downlink path.

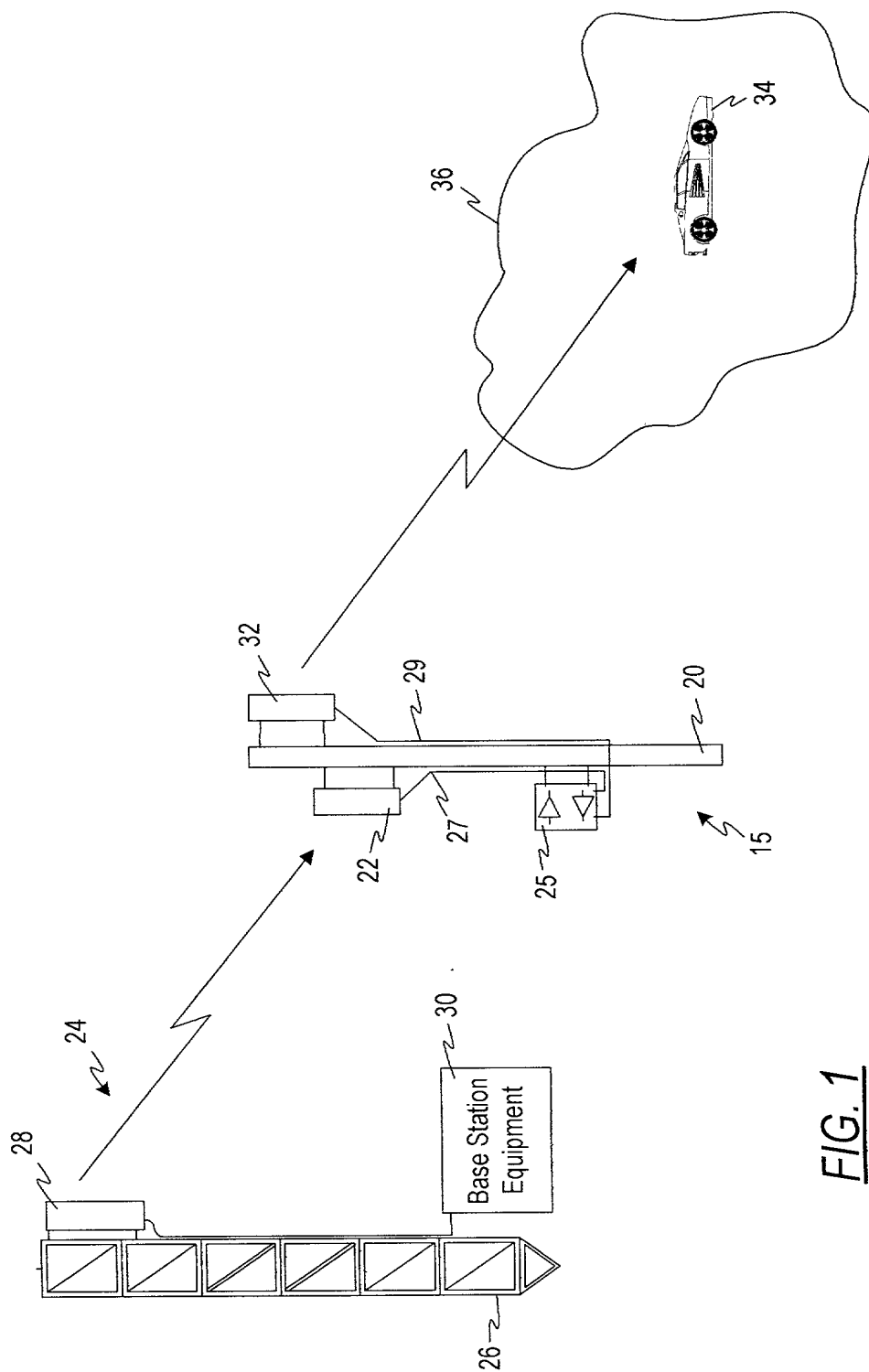


FIG. 1
Prior Art

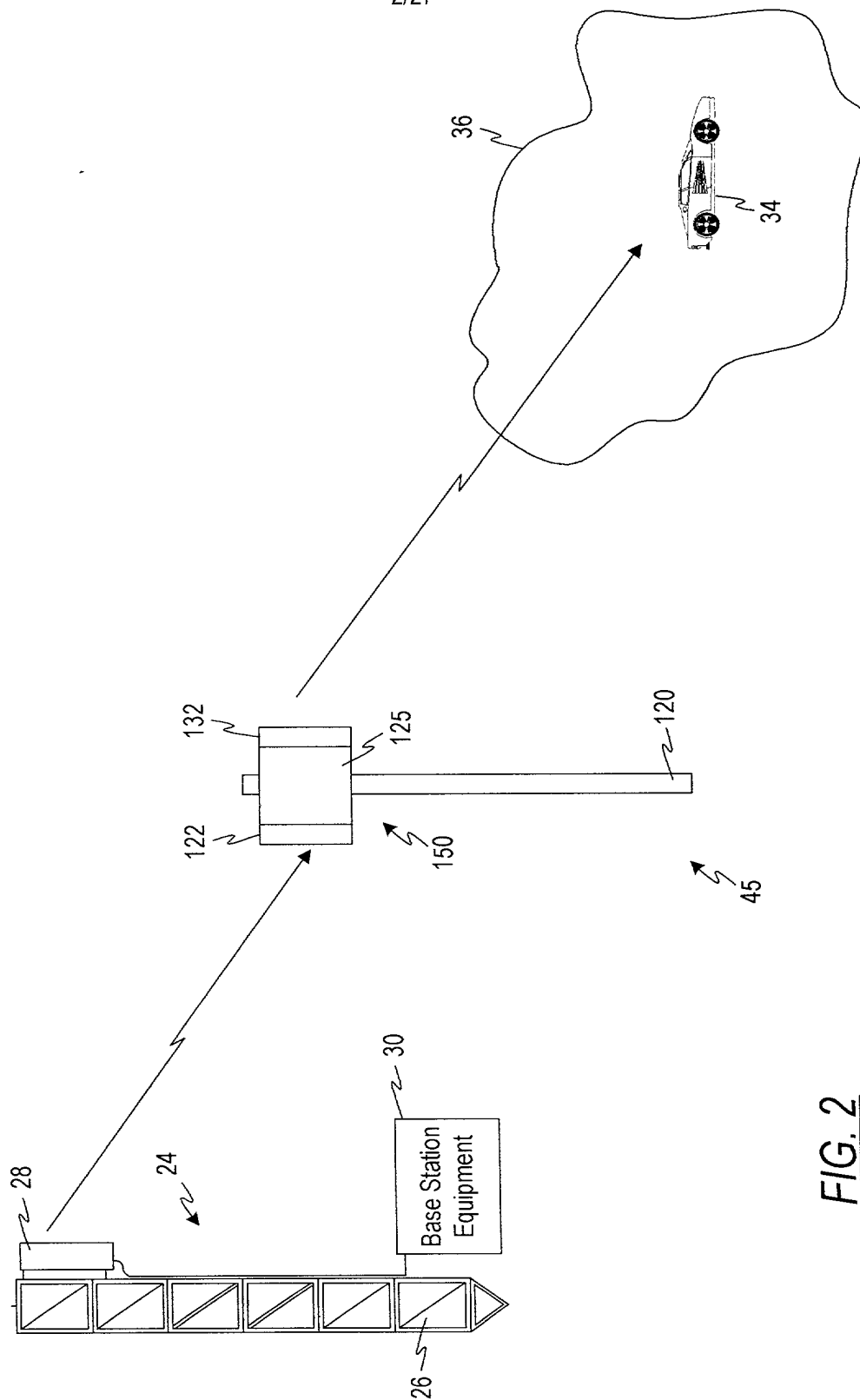


FIG. 2

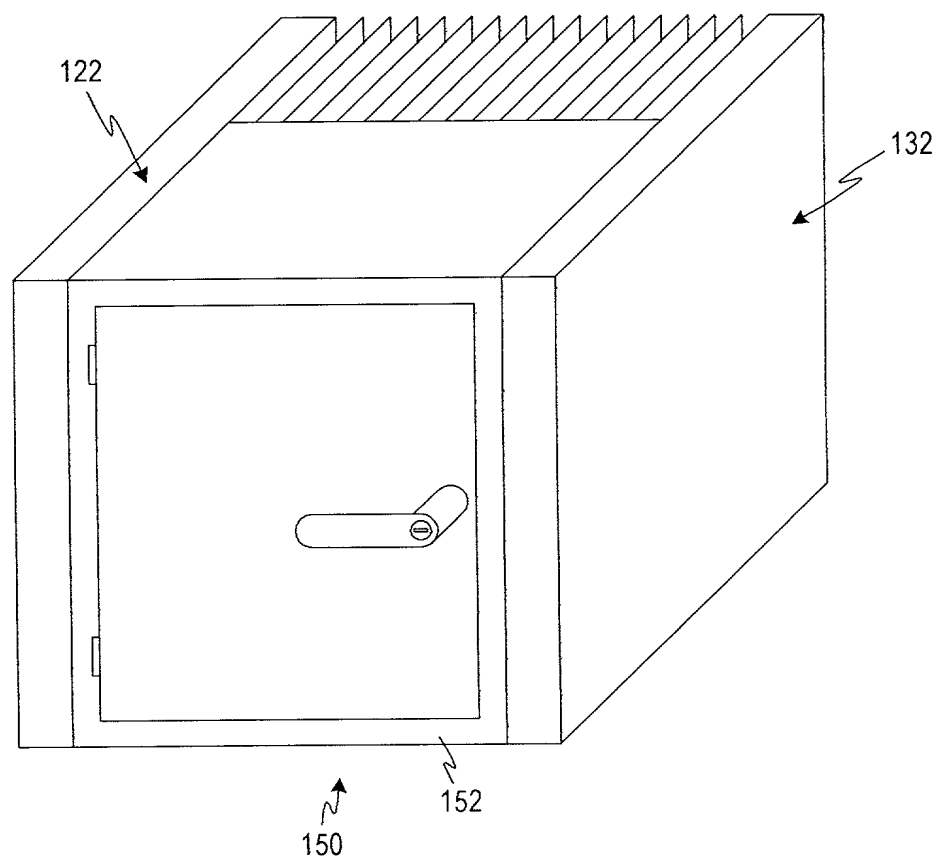


FIG. 3

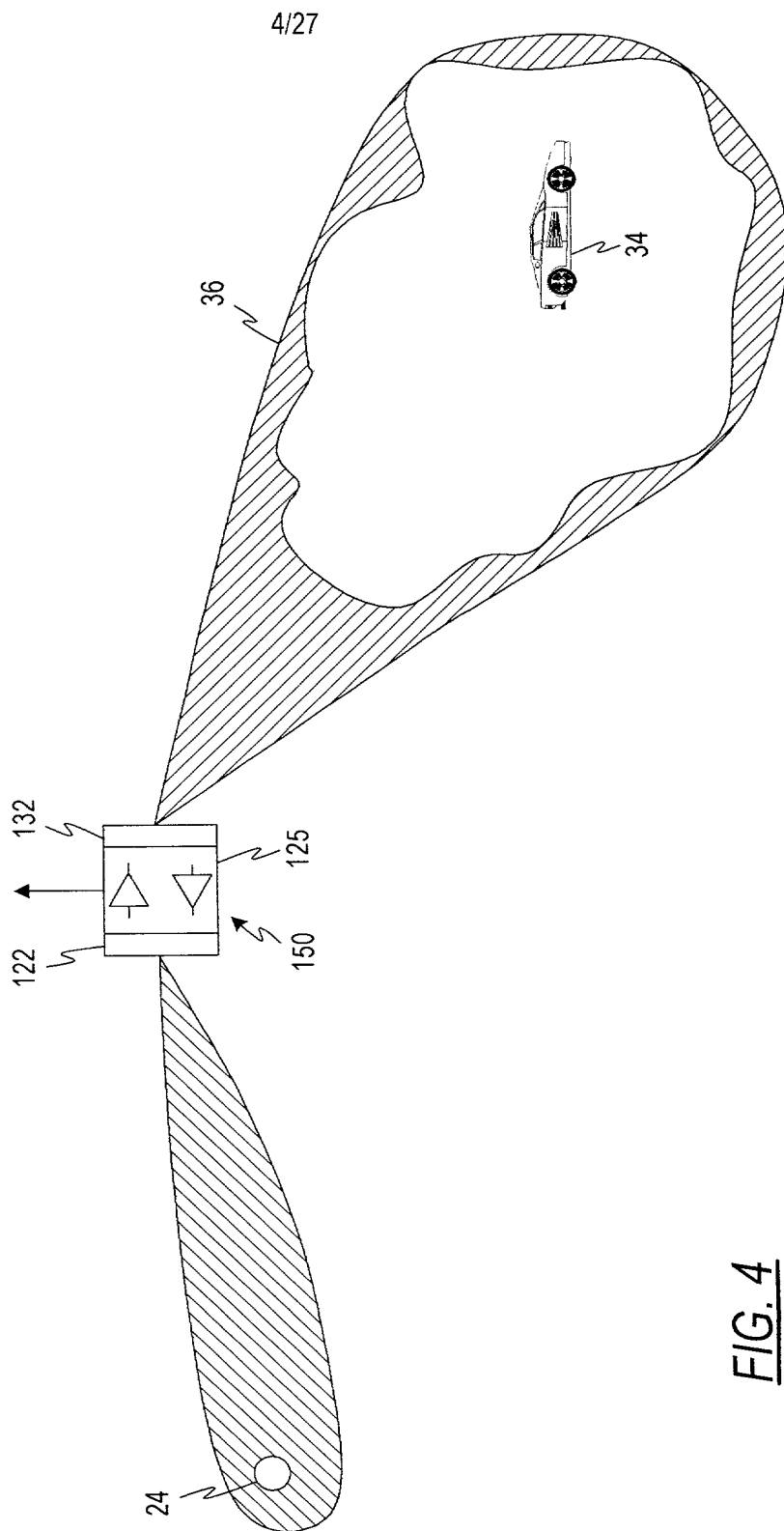


FIG. 4

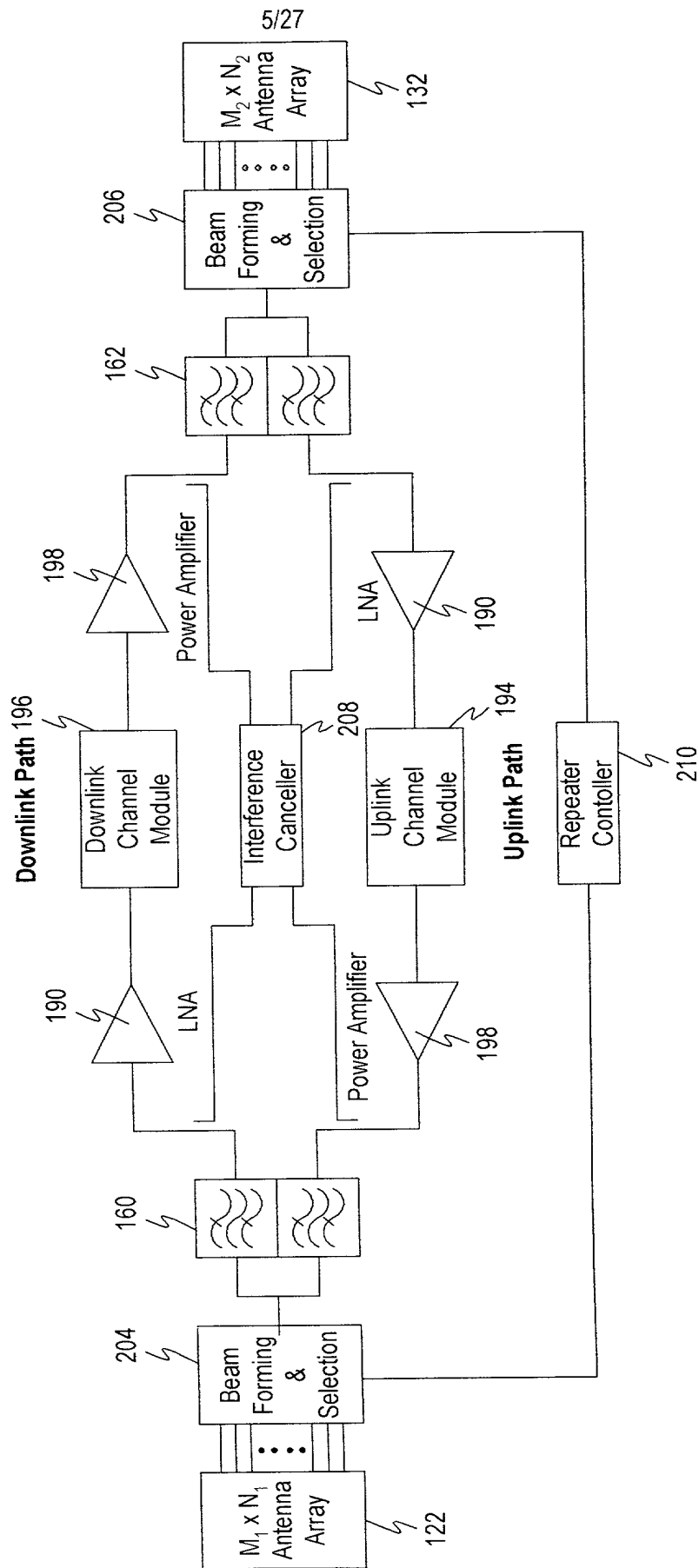


FIG. 5

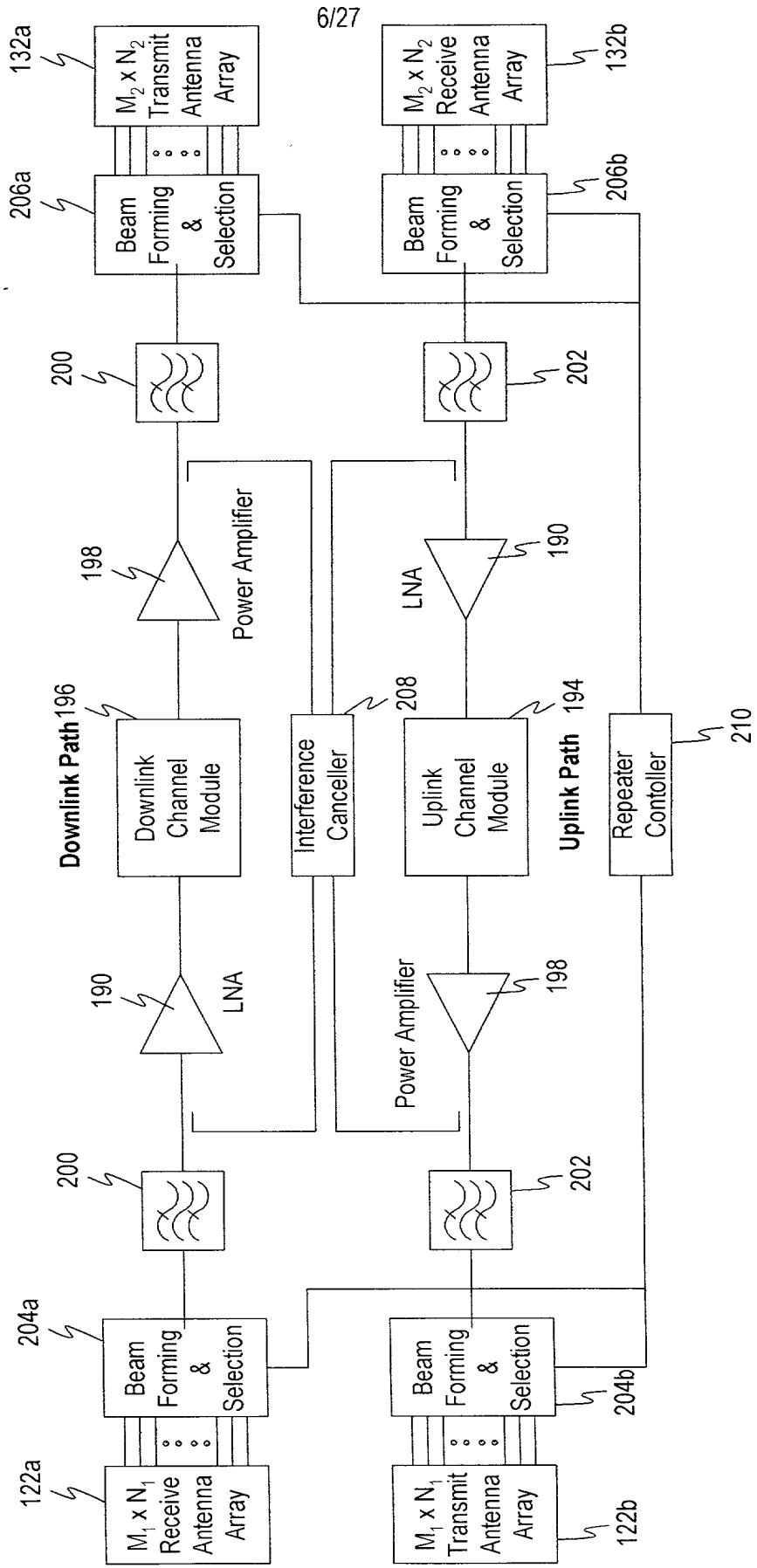


FIG. 6

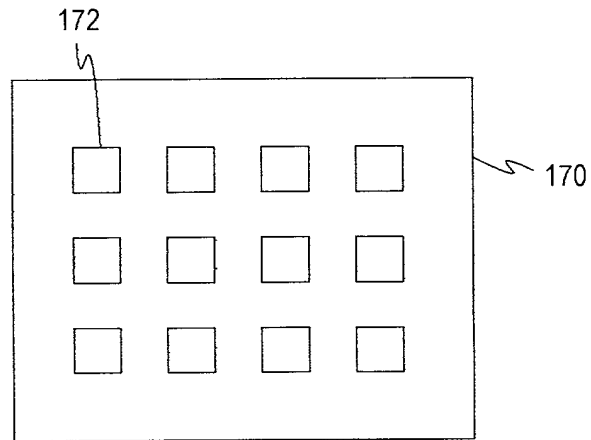


FIG. 7

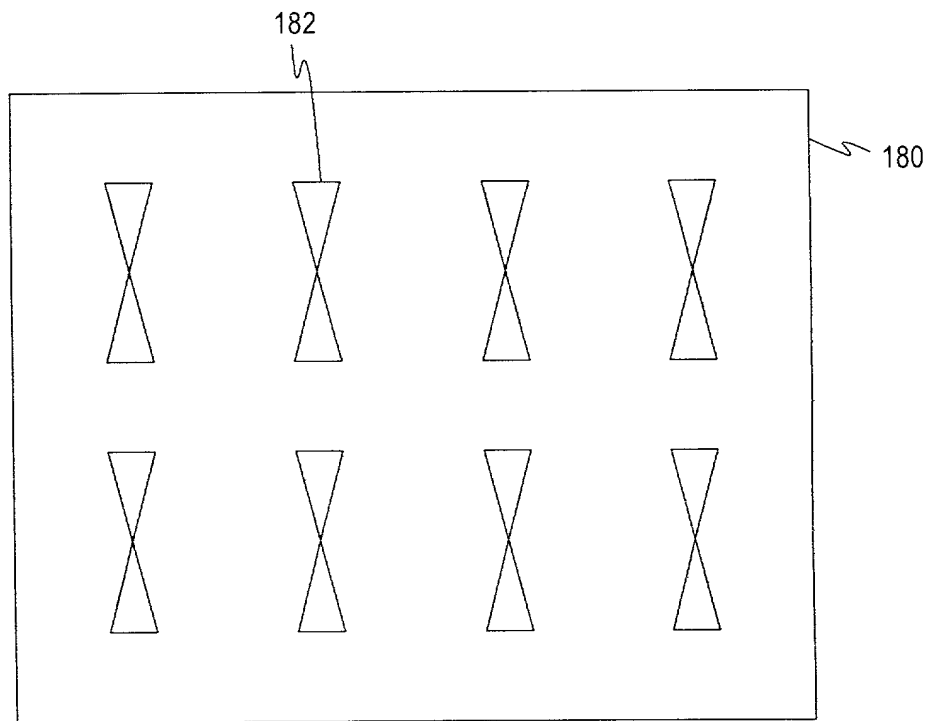
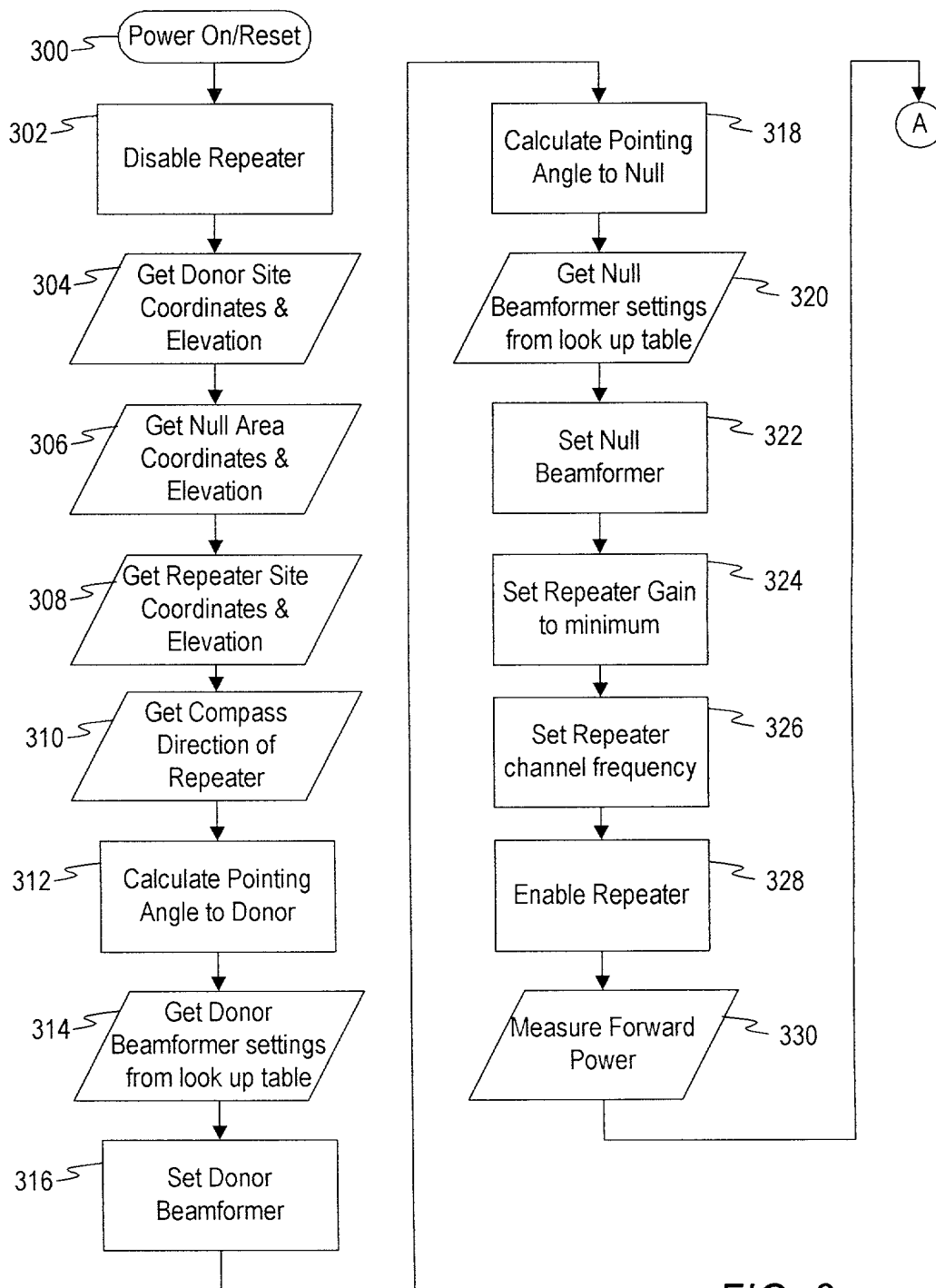
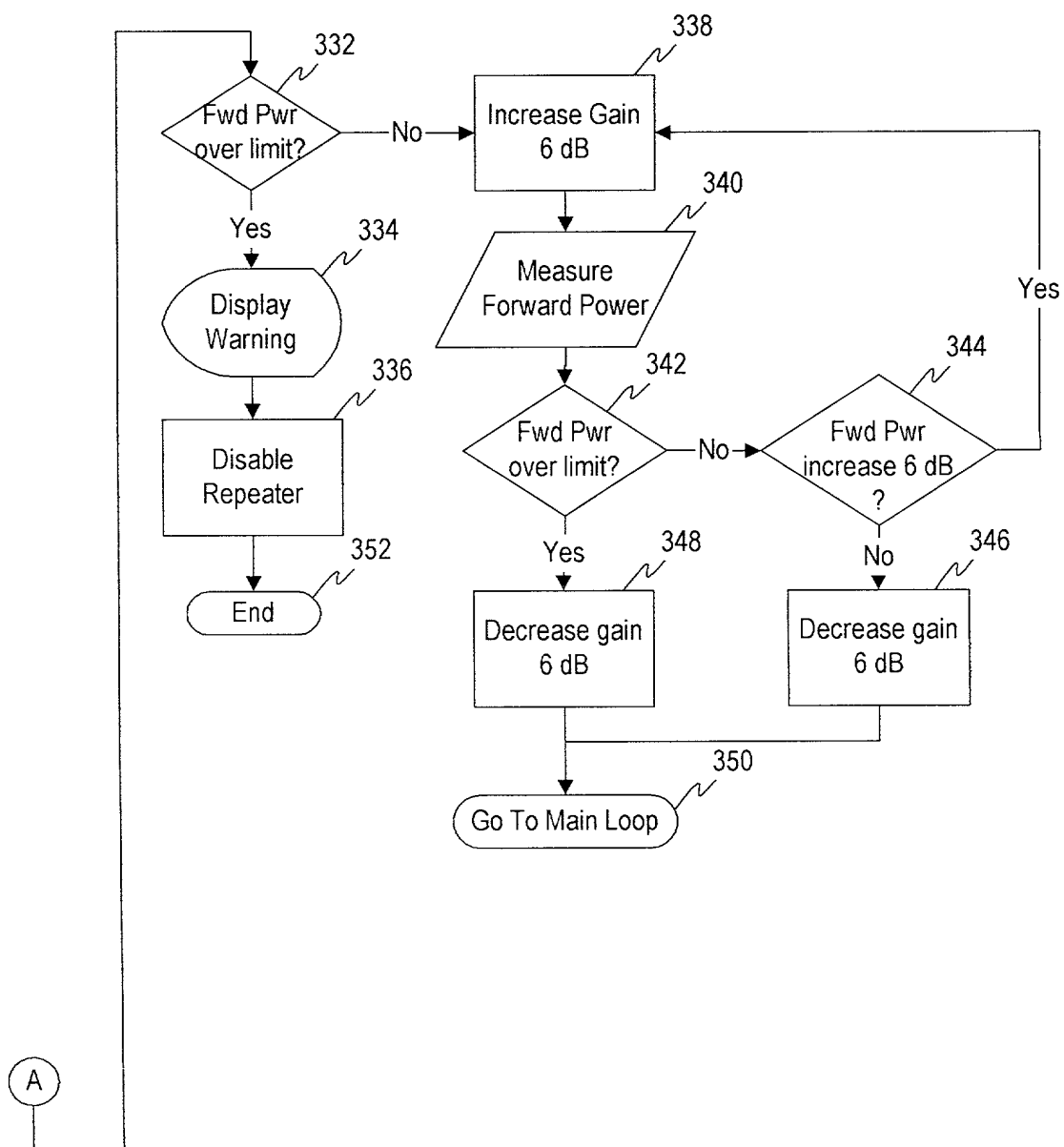


FIG. 8

FIG. 9a

FIG. 9b

10/27

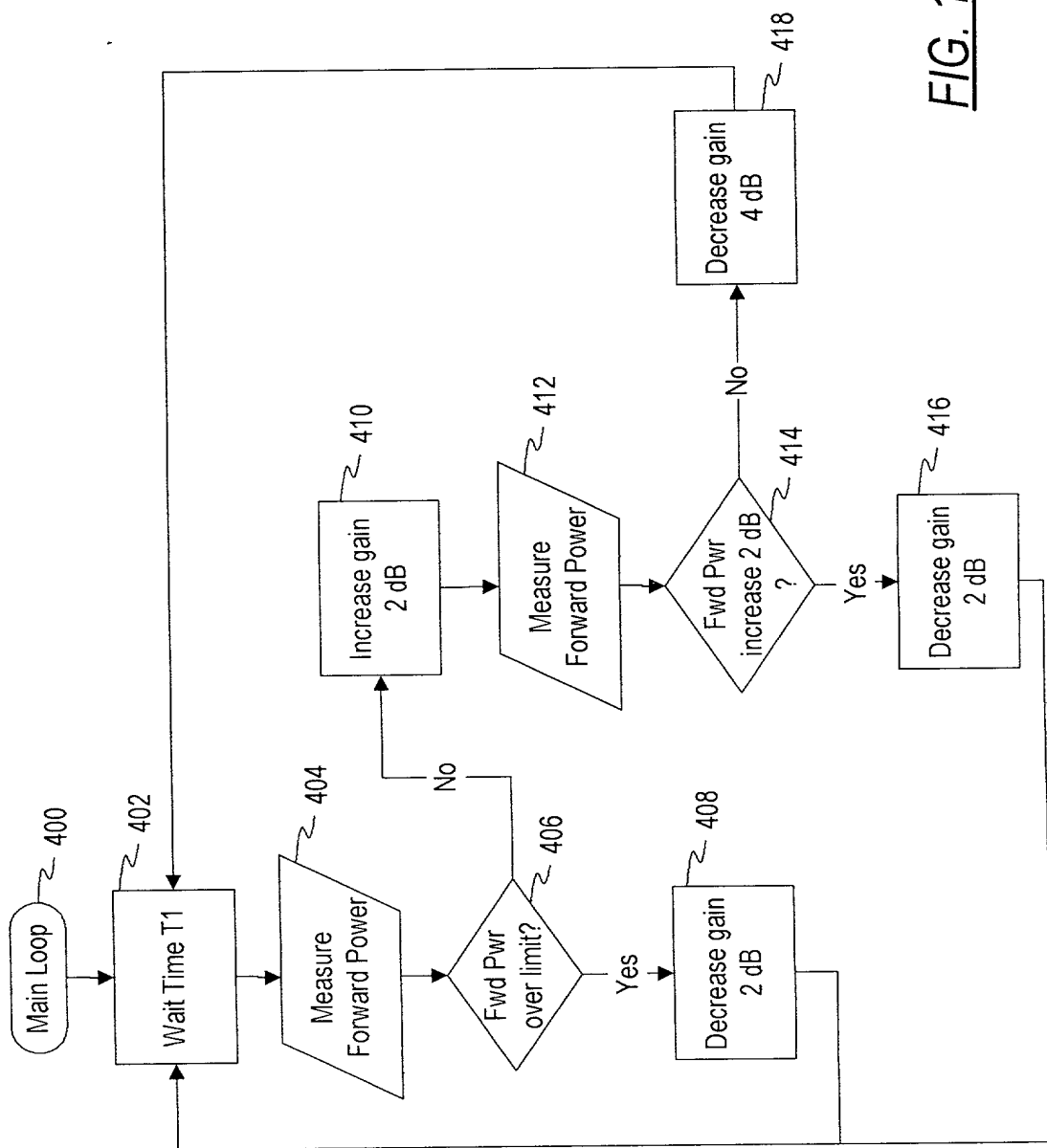


FIG. 10

11/27

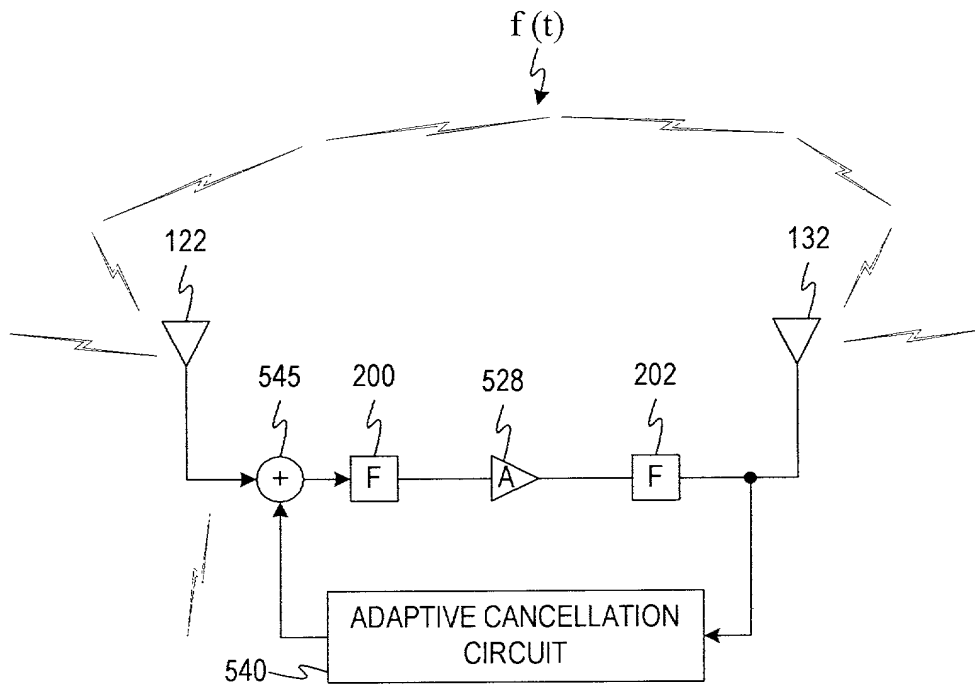


FIG. 11

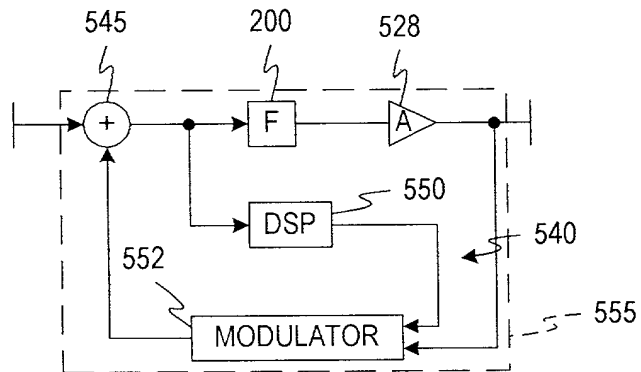


FIG. 12

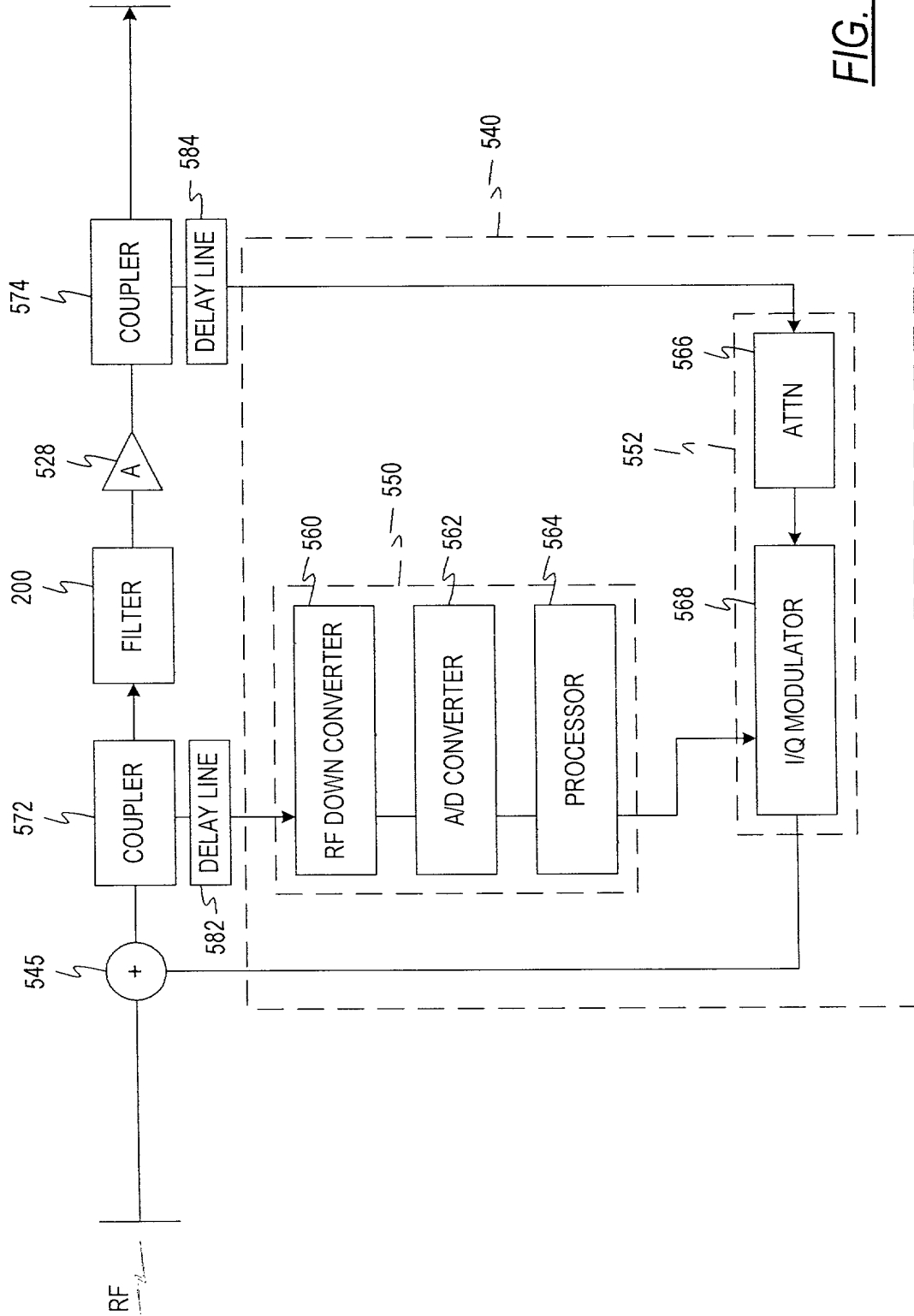


FIG. 13

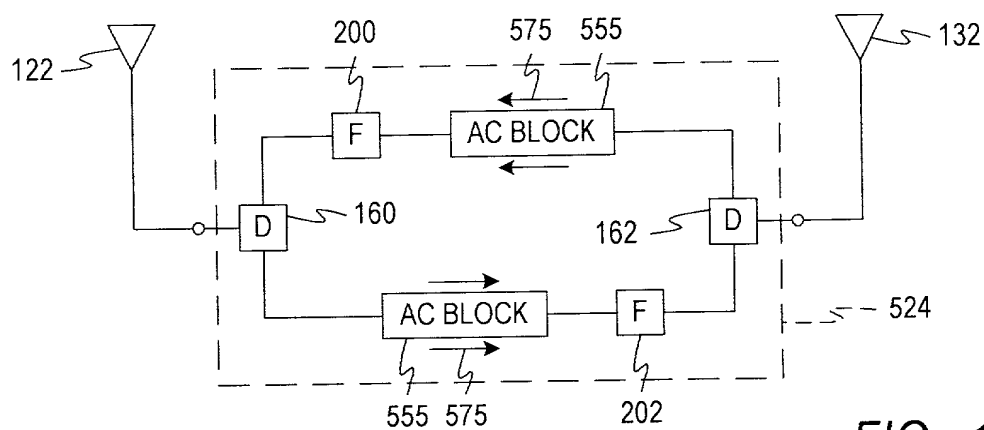


FIG. 14

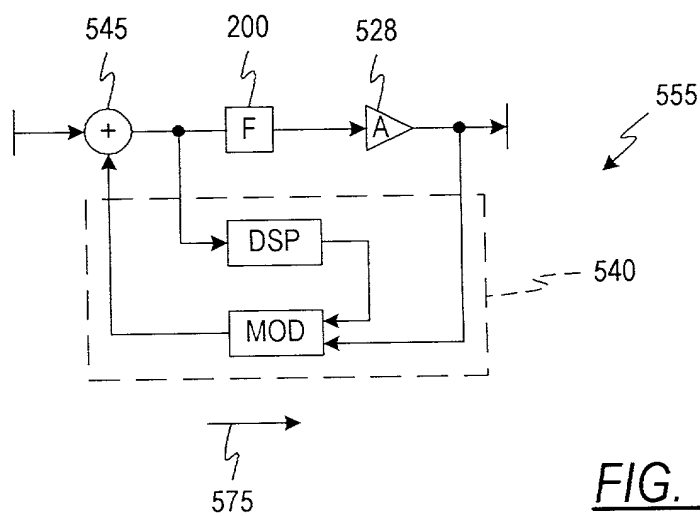


FIG. 15

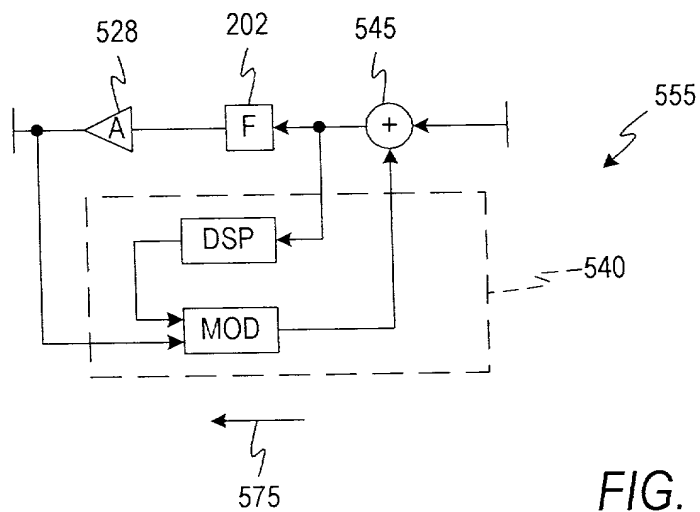
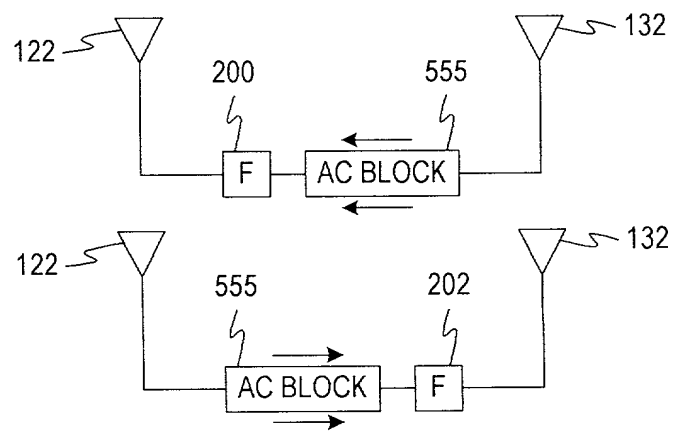
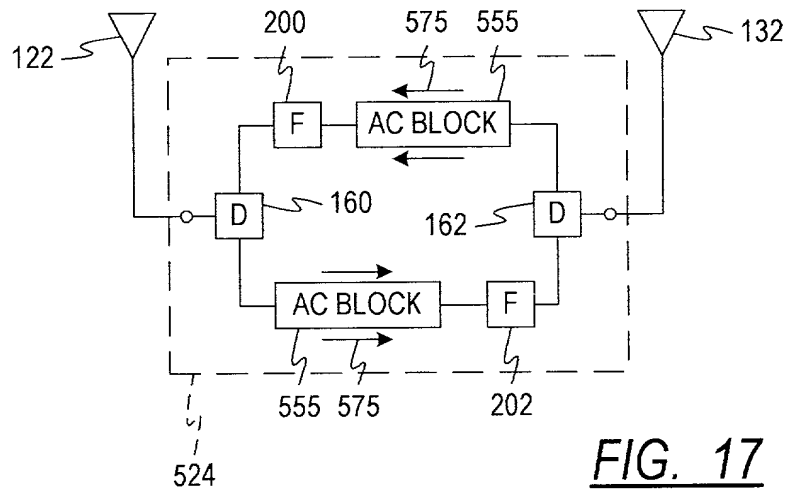


FIG. 16



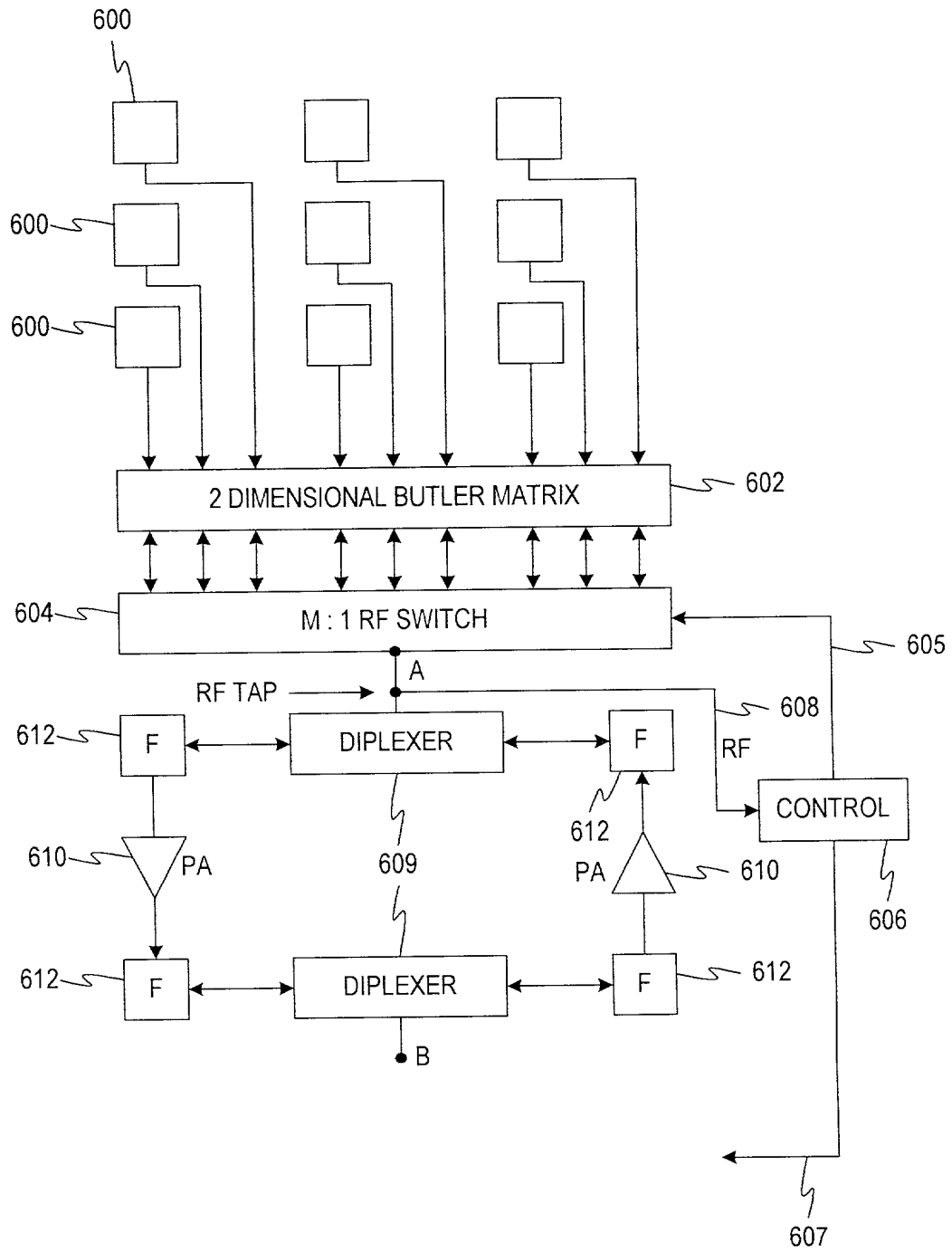
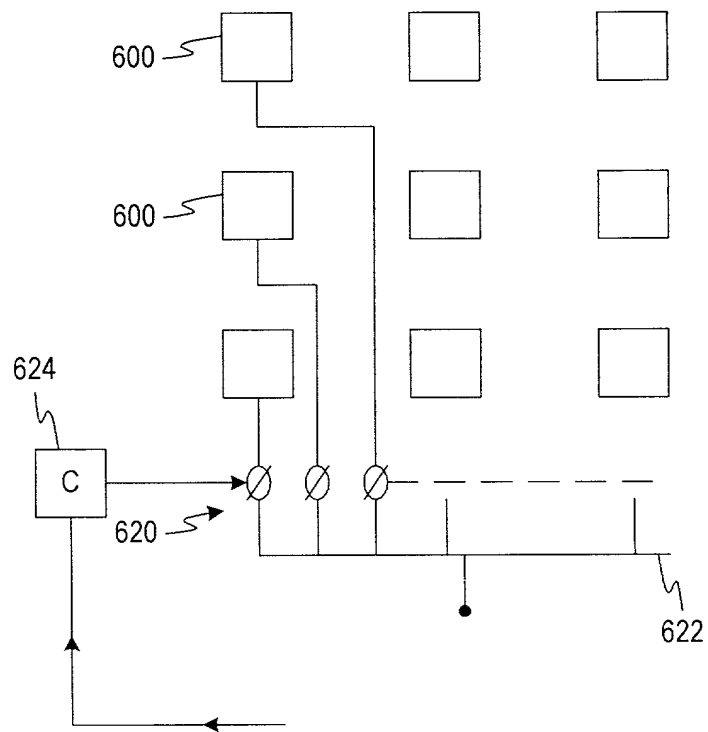


FIG. 19

FIG. 20

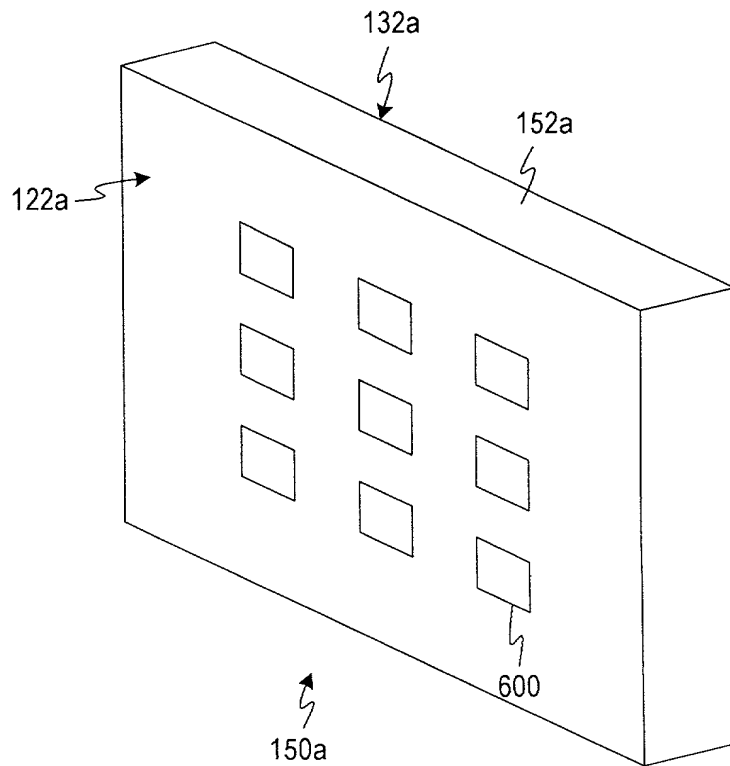
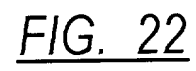


FIG. 21



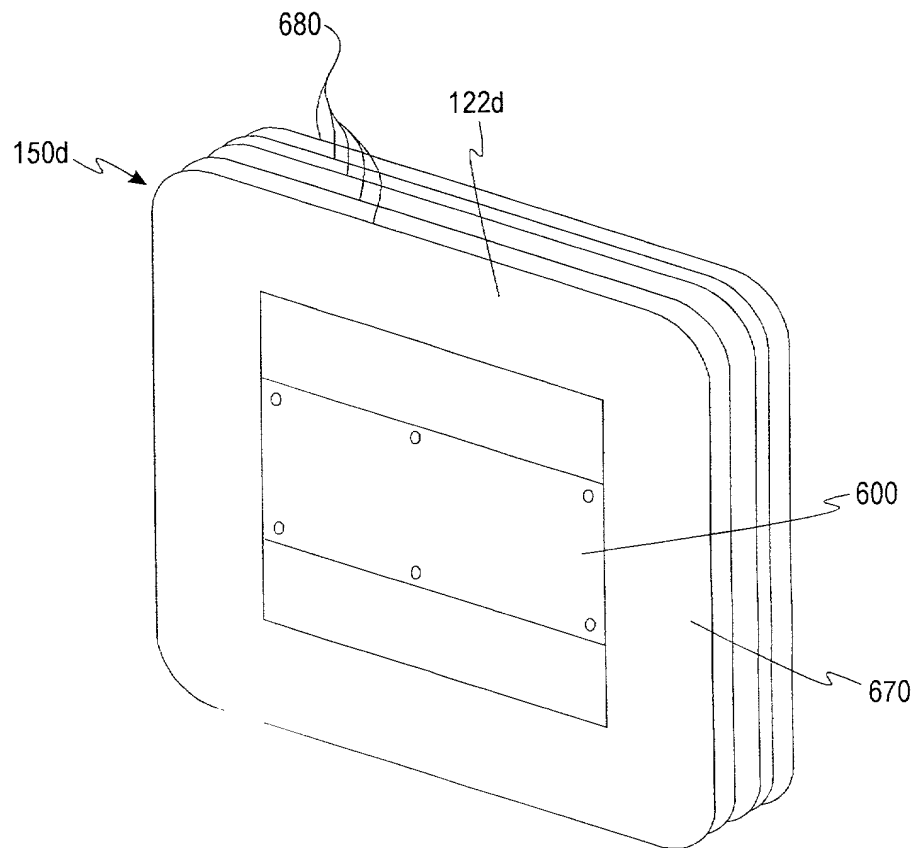


FIG. 23

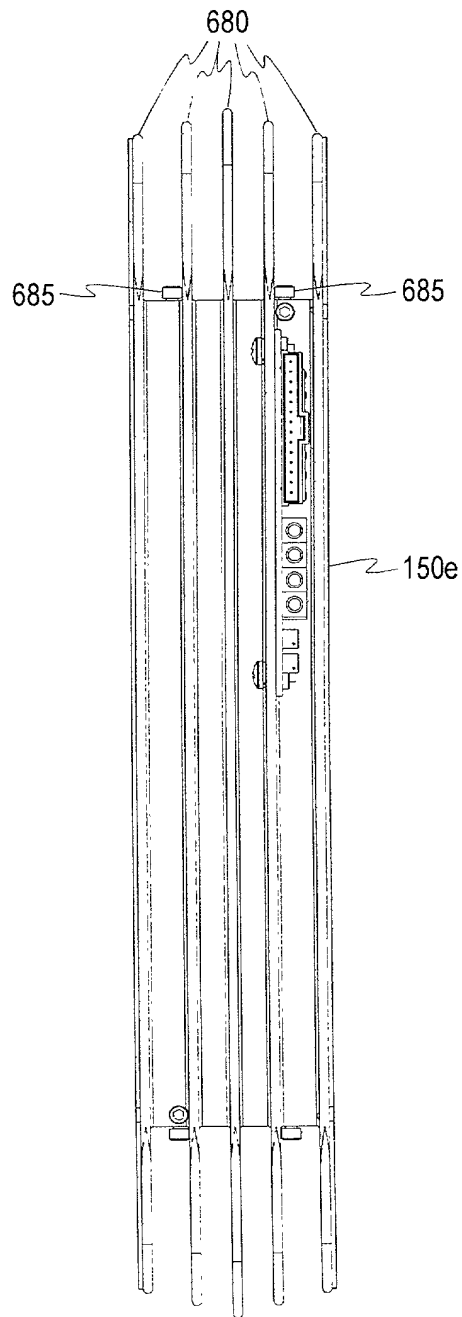


FIG. 24

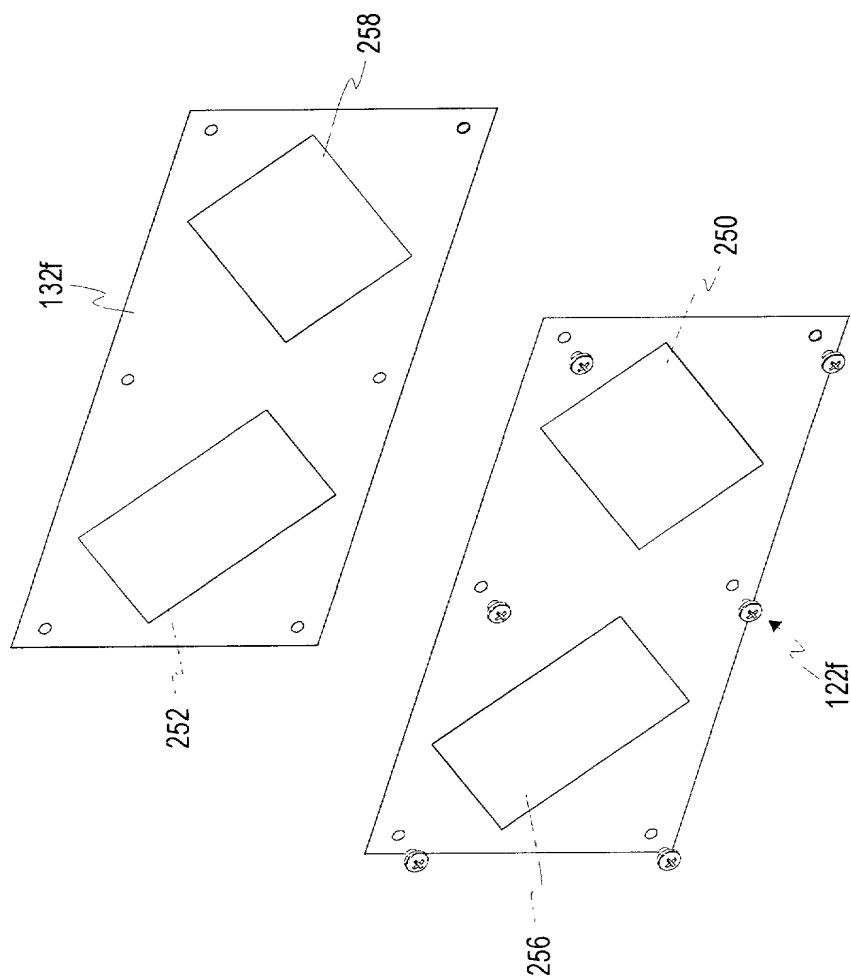


FIG. 25

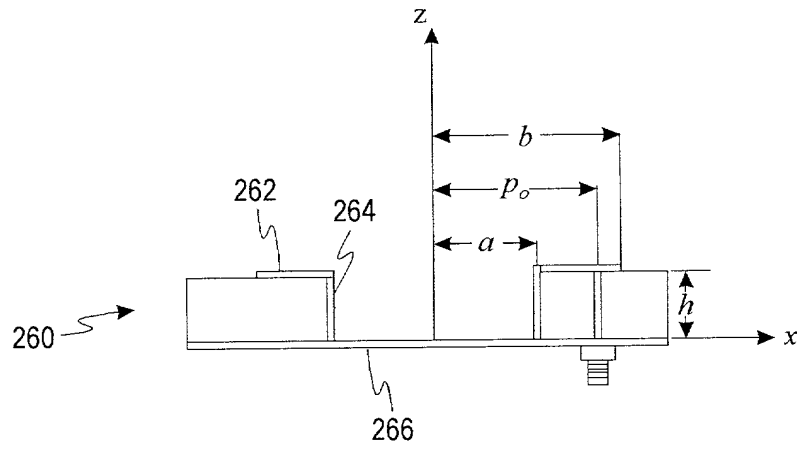


FIG. 26

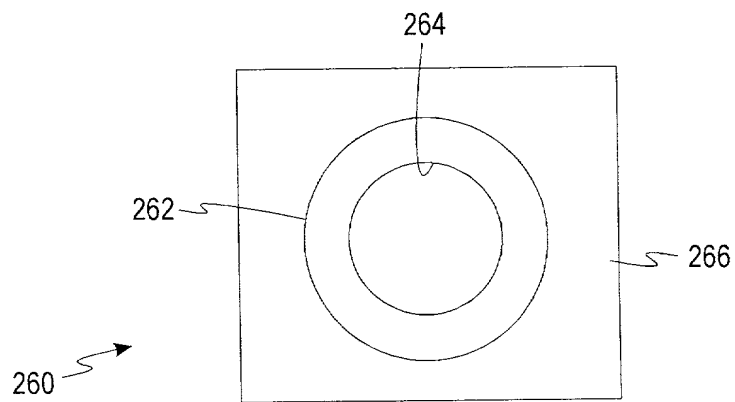


FIG. 27

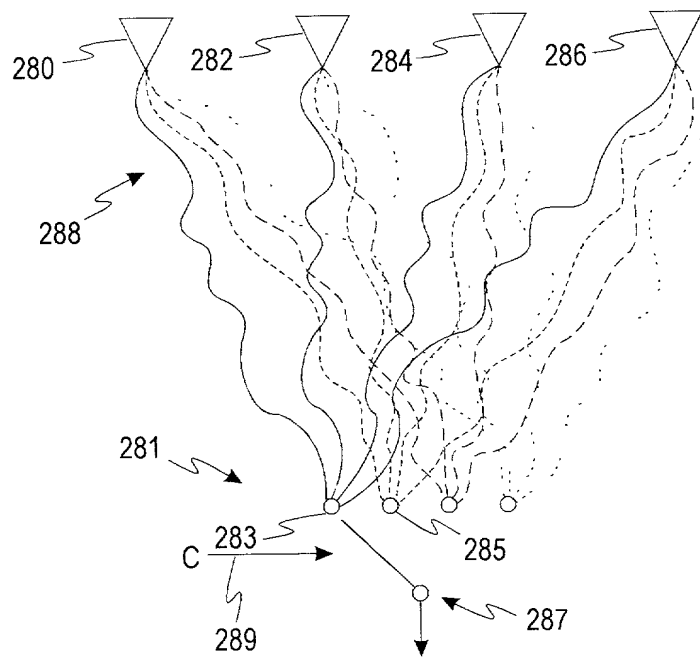


FIG. 28

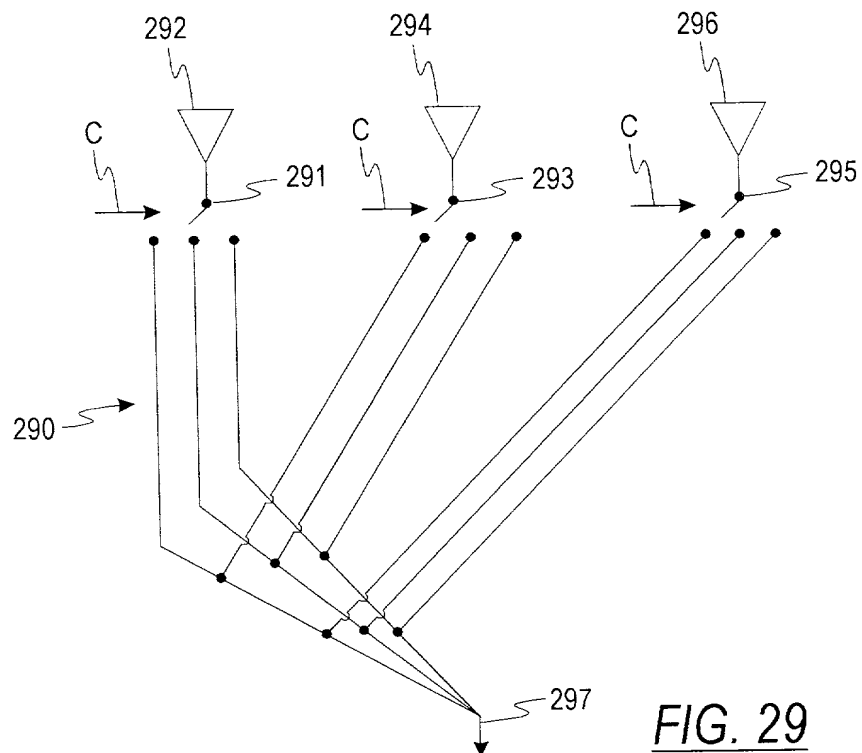


FIG. 29

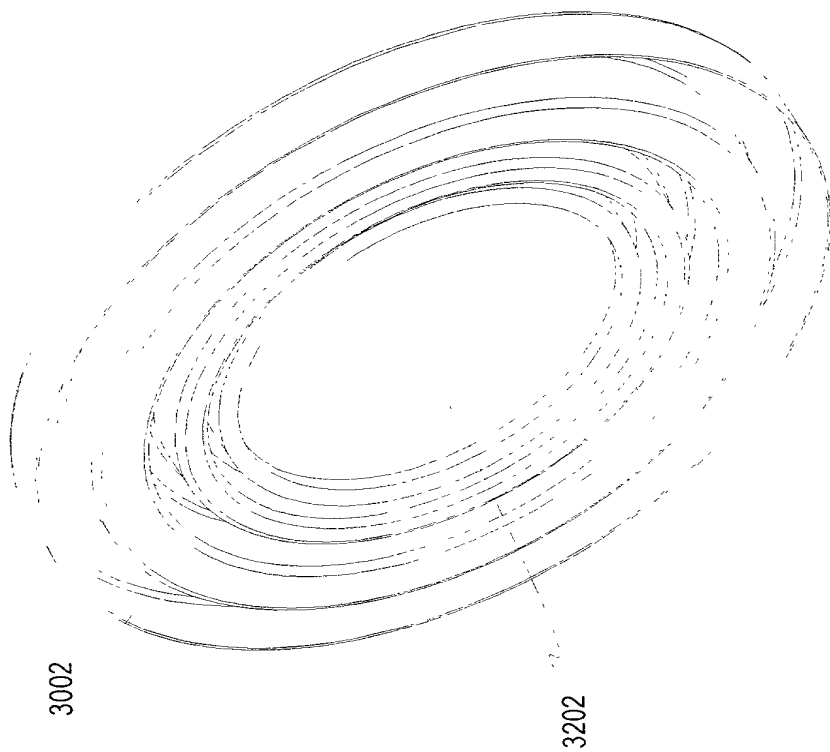


FIG. 30

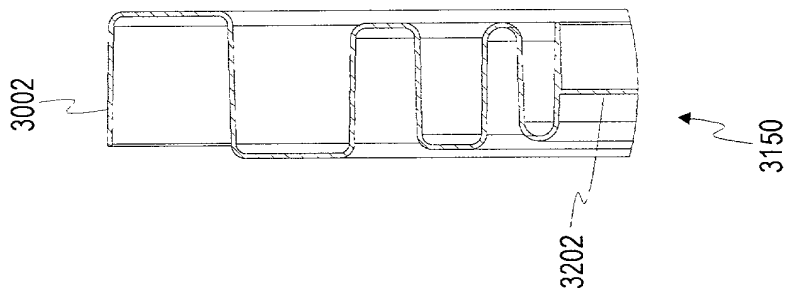


FIG. 31

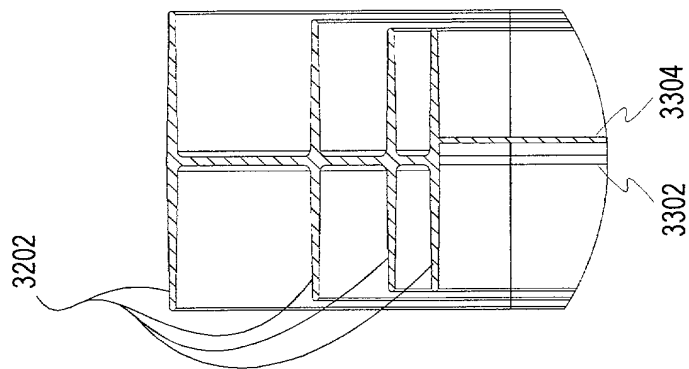
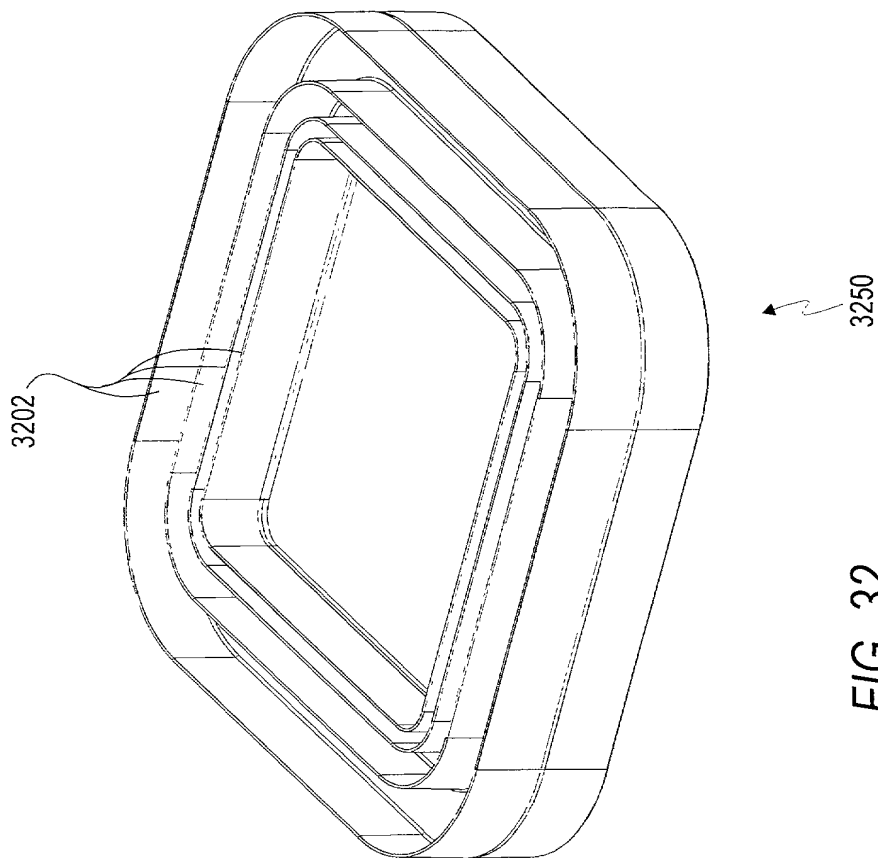
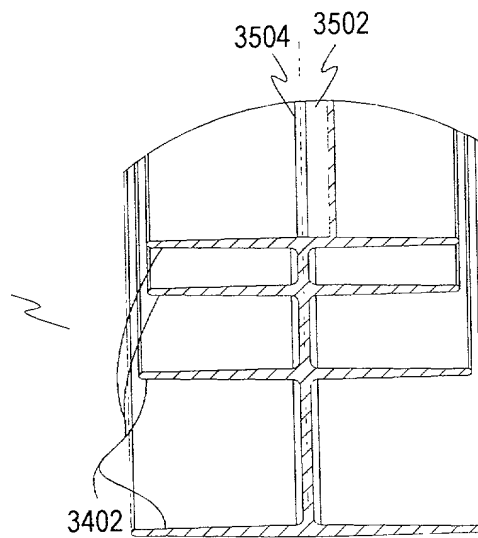
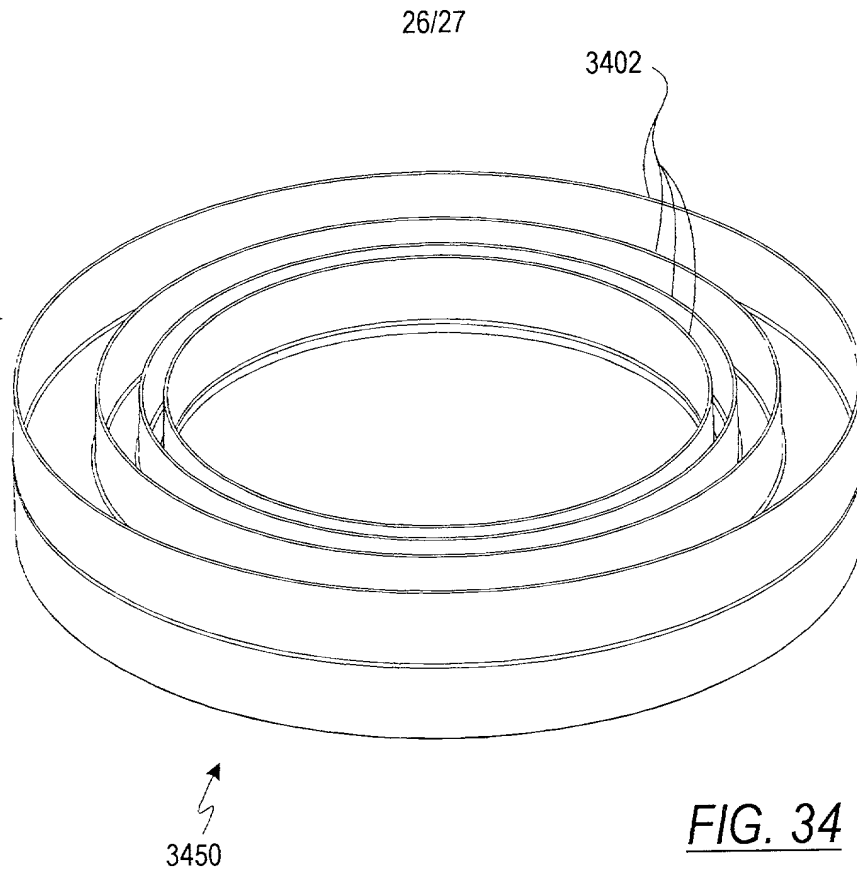
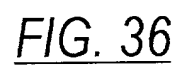


FIG. 33

FIG. 32





**RULES 63 AND 67 (37 C.F.R. 1.63 and 1.67)
DECLARATION AND POWER OF ATTORNEY**

FOR UTILITY/DESIGN/CIP/PCT NATIONAL APPLICATIONS

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name; and

I believe that I am an original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: **INTEGRATED REPEATER**, the specification of which:

- X (a) is attached hereto.
____ (b) was filed on _____ as Application Serial No. _____
and was amended on _____ (if applicable)
____ (c) was filed as PCT International Application No. PCT/_____ on
_____ and was amended on _____ (if applicable).
____ (d) was filed on _____ as Application Serial No _____
and was issued a Notice of Allowance on _____.
____ (e) was filed on _____ and bearing attorney docket number _____

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims as amended by any amendment referred to above or as allowed as indicated above.

I acknowledge the duty to disclose all information known to me to be material to the patentability of this application as defined in 37 CFR § 1.56. If this is a continuation-in-part (CIP) application, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose to the Office all information known to me to be material to patentability of the application as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

I hereby claim foreign priority benefits under 35 U.S.C. § 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate filed by me or my assignee disclosing the subject matter claimed in this application and having a filing date (1) before that of the application on which my priority is claimed or, (2) if no priority is claimed, before the filing date of this application:

PRIOR FOREIGN PATENTS

<u>Number</u>	<u>Country</u>	<u>Month/Day/ Year Filed</u>	<u>Date first laid- open or Published</u>	<u>Date patented or Granted</u>	<u>Priority Claimed</u>	
					<u>Yes</u>	<u>No</u>
NONE						

I hereby claim the benefit under 35 U.S.C. § 120/365 of any United States application(s) listed below and PCT international applications listed above or below:

PRIOR U.S. OR PCT APPLICATIONS

09/357,032	July 20, 1999	Pending
(Application Serial No.)	(Filing Date)	(Status)
09/483,234	January 14, 2000	Pending
(Application Serial No.)	(Filing Date)	(Status)
09/483,649	January 14, 2000	Pending
(Application Serial No.)	(Filing Date)	(Status)

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all of the firm of **JENKENS & GILCHRIST, a Professional Corporation**, 1445 Ross Avenue, Suite 3200, Dallas, Texas 75202-2799, as my attorneys and/or agents, with full power of substitution and revocation, to prosecute this application, provisionals thereof, continuations, continuations-in-part, divisionals, appeals, reissues, substitutions, and extensions thereof and to transact all business in the United States Patent and Trademark Office connected therewith, to appoint any individuals under an associate power of attorney and to file and prosecute any international patent application filed thereon before any international authorities, and I hereby authorize them to act and rely on instructions from and communicate directly with the person/assignee/attorney/firm/organization who/which first sent this case to them and by whom/which I hereby declare that I have consented after full disclosure to be represented unless/until I instruct them in writing to the contrary.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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